



SPECIFICATION

Edition

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EPR MOS RELAY (4PIN)

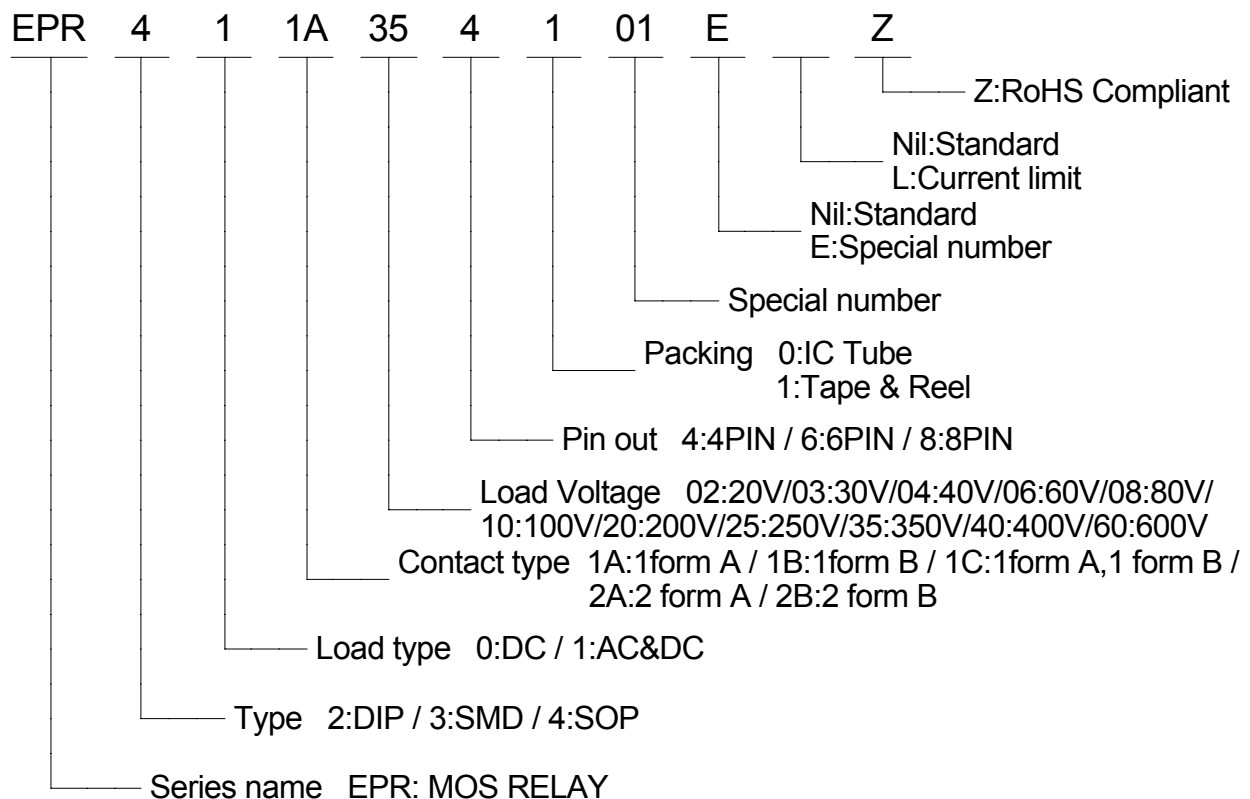
1. FEATURES :

- 1.1 No EMI/RFI Generation
- 1.2 High reliability
- 1.3 No moving parts
- 1.4 Low drive power requirement (TTL/CMOS Compatible)
- 1.5 Low On-state Resistance
- 1.6 3750 Voltage input/output isolation(1500V isolation for SO-package)
- 1.7 Arc-free with no snubbing circuits
- 1.8 Machine insertable or wave solderable

2. APPLICATION :

- 2.1 Telecommunications
- 2.2 Instrumentation
- 2.3 Medical equipment
- 2.4 Security
- 2.5 Industrial control

3. PART NUMBERING SYSTEM :



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4. SPECIFICATION :

Part Number(4PIN)	Marking	PACKING	Contact Form
EPR411A354101EZ SOP / 350VAC&DC	41A35S1	2000 pcs/Tape & reel	1A

Electrical characteristics(Ambient temperature:25)

ITEM		Symbol	MIN.	TYP	MAX	UNIT	note
Input	Forward voltage	V _F	1.0		1.4	V	I _F =10mA
	Reverse current	I _R			10	μA	V _R =5V
	Control current	I _{Fopr}	5		50	mA	
Output	Load voltage (AC peak or DC)	V _L	350			V	I _D =100μA
	Continuous Rated Load Current	SOP	I _L		80	mA	I _F =10mA
		DIP/SMD			---		
	Peak current	SOP	I _{Lpeak}		200	mA	10ms
		DIP/SMD			---		
	On-state resistance	R _{ON}		40	50	Ohm	I _F =10mA,I _L =rating
	Off-state Leakage current	I _{LK}			1	μA	I _F =0mA,V _L =rating
	Turn-On Time	T _{ON}		0.3	1.5	ms	I _F =10mA,V _L =rating, I _L =rating
	Turn-Off Time	T _{OFF}		0.03	1	ms	I _L =rating
Output Capacitance	C _{OUT}			---	pF	f=1MHz	
Input/ Output	I/O Capacitance	C _{I/O}		1		pF	f=1MHz
	I/O Isolation voltage	SOP	V _{I/O}	1500		VAC	R.H. 60% 1min
		DIP/SMD		---			
I/O Isolation resistance	R _{I/O}	5			GΩ	DC=500V delay 2sec	
Temperature limits	Operating	T _{OP}	-40 to +85 (-40 to +185)				
	Storage	T _{STG}	-40 to +100 (-40 to +212)				

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RECOMMENDED OPERATING CONDITIONS

CHARACTERISTIC	SYMBOL	MIN.	TYP.	MAX.	UNIT
Forward Current	IF	5	10	25	mA
Operating Temperature	T _{opr}	-20		65	

5. PACKING :

5.1 PACKING METHOD

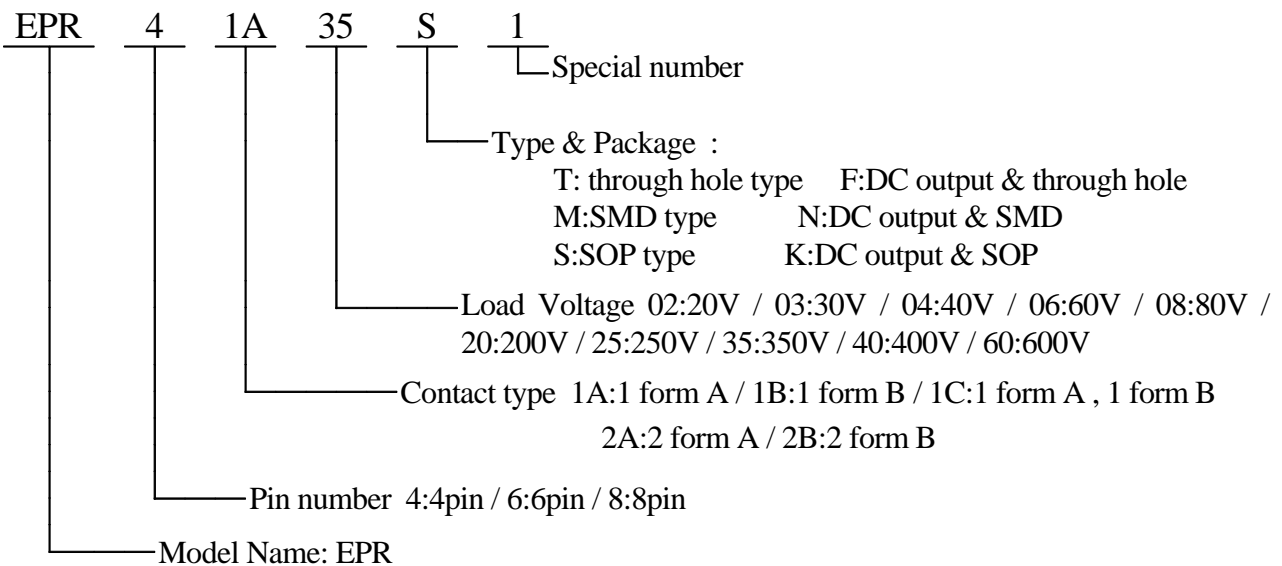
All relays are packed into IC tubes as an inner packing unit, and ten IC tubes bundled together with rubber bands.

5.2 Relays may also be packed using tape & reel methods.

5.3 INFORMATION ON LABEL (stuck to each bundle of IC tubes).

- | | |
|-----------------|--------------|
| (1) DESCRIPTION | (5) ECE MARK |
| (2) LOT NO. | (6) QC STAMP |
| (3) QUANTITY. | (7) DATE. |
| (4) TESTER NO. | |

6. MARKING SYSTEM:



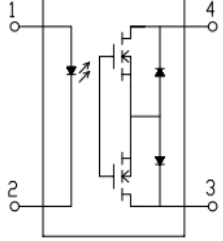
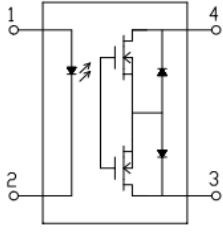
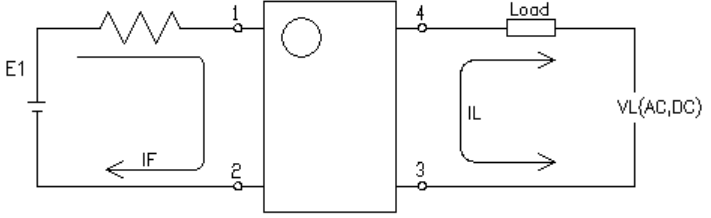
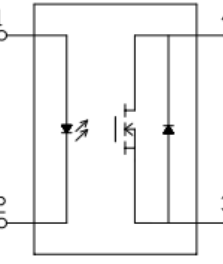
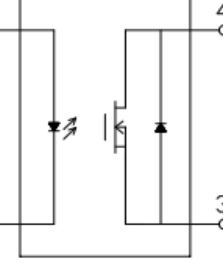
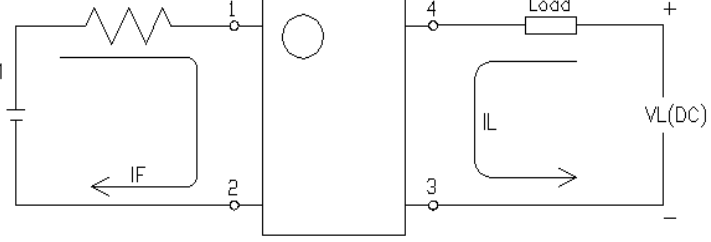
* 4 pin products without "EPR" mark.

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7. CIRCUIT DIAGRAM & APPLICATION:

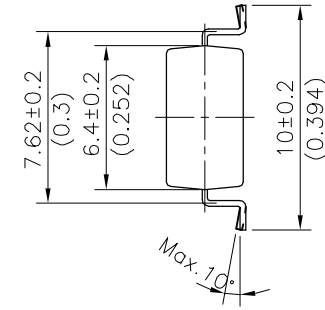
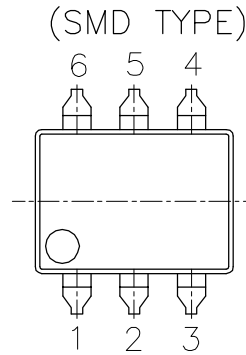
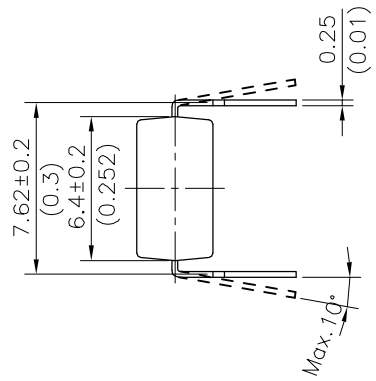
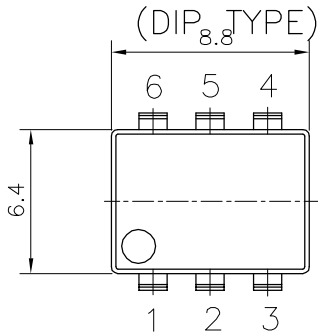
Circuit diagram(4 PIN)	Load type	Application
<p>EPR 4P 1A TYPE (AC/DC)</p>  <p>EPR 4P 1B TYPE (AC/DC)</p> 	<p>AC&DC</p>	
<p>EPR 4P 1A TYPE (DC)</p>  <p>EPR 4P 1B TYPE (DC)</p> 	<p>DC</p>	

8. DRAWINGS :

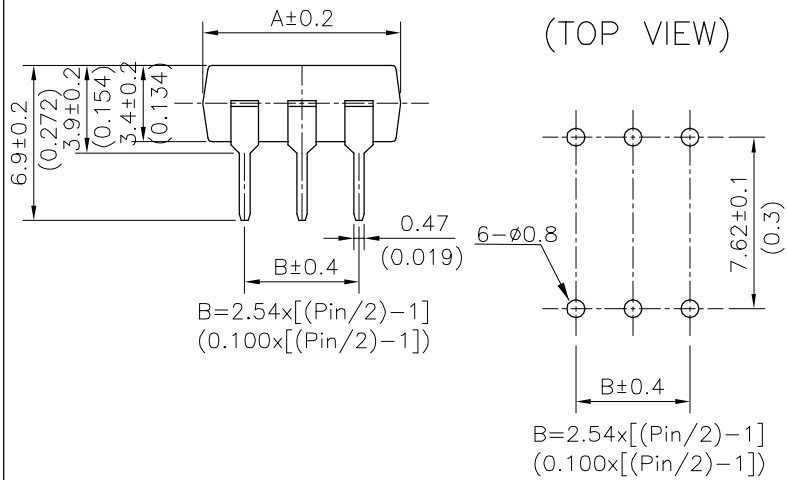
Dimensions & P.C.B. layout

see attached drawing.

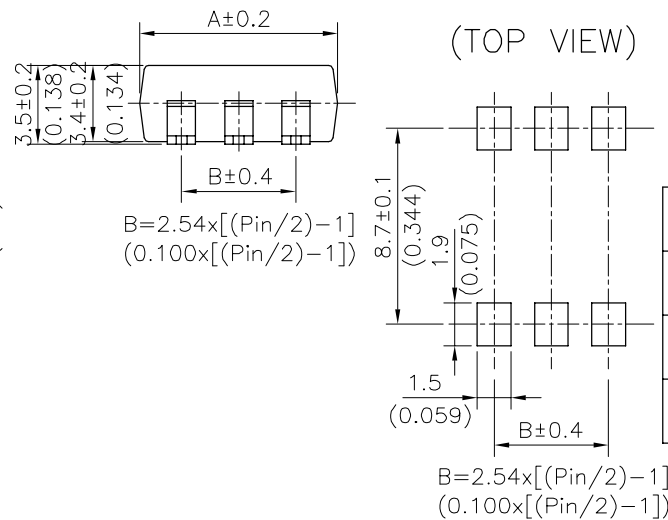
DIMENSIONS Unit:mm(inch)



P.C.B. LAYOUT
(TOP VIEW)



P.C.B. LAYOUT
(TOP VIEW)



Pin out	Dimensions A	Dimensions B
4 Pin	4.7(0.185)	2.54(0.100)
6 Pin	8.8(0.346)	5.08(0.200)
8 Pin	9.78(0.385)	7.62(0.300)

NO.	DETAILS	ALTERED BY	DATE	NORMAL TOLERANCE		PART NUMBER	UNIT	MM(INCH)	PART NAME	EPR
				RANGE	TOLERANCE					
				0 - 1	±0.1	7332*****00(DIP) 7333*****00(SMD)				
				1 - 4	±0.3	CHECK CCY DESIGN CCY DRAWN JASON	SCALE	3 : 1	TYPE	DIP,SMD
				4 - 16	±0.5		QUANTITY	-----	FILE NAME	7332xxxxxx00A.DWG
				16 - 63	±0.8		PROCESSING	-----	EDITION	B
				63 - 250	±1.0		PROJECTION		SURFACE TREATMENT	-----