# TLP185 Technical Information

This material is technological examination material to aim at the product introduction. The change in the content of the characteristic might be accompanied at the final specification process. The final specification will be able to be gotten in the brokerage department when the product is designed and to get the confirmation.

15<sup>th</sup>. July 2011

Toshiba Corporation Semiconductor & Storage Products Company Optoelectronics Device Marketing & Engineering Group 1 TOSHIBA Photocoupler GaAs Ired & Photo-Transistor

## **TLP185**

Office Machine
Programmable Controllers
AC Adapter
I/O Interface Board

The TOSHIBA mini flat coupler TLP185 is a small outline coupler, suitable for surface mount assembly.

TLP185 consist of a photo transistor optically coupled to a gallium arsenide infrared emitting diode. Since TLP185 is smaller than DIP package, it's suitable for high-density surface mounting applications such as programmable controllers

Collector-emitter voltage: 80V (min)Current transfer ratio: 50% (min)

Rank GB: 100% (min)

Isolation voltage: 3750Vrms (min)
 Operation Temperature:-55 to 110 °C

Safety Standards

UL(under application): UL1577, File No. E67349

cUL(under application): CSA Component Acceptance Service No. 5A

File No.E67349

BSI(under application): BS EN60065:2002, certificate No.8285

BS EN60950-1:2006 certificate No.8286

• Option (V4) type

VDE (under application): EN60747-5-2

(Note): When a EN60747-5-2 approved type is needed,

Please designate "Option(V4)"

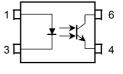
Construction mechanical rating

Creepage distance : 5.0 mm(min)
Clearance : 5.0 mm(min)
Insulation thickness : 0.4 mm(min)

Unit: mm

Weight: 0.08 g (Typ.)

#### Pin Configuration(top view)



- 1: Anode
- 3: Cathode
- 4: Emitter
- 6: Collector



#### **Current Transfer Ratio**

Type	Classification	(I <sub>C</sub>	sfer Ratio (%) / I <sub>F</sub> ) = 5V, Ta = 25°C	Marking Of Classification
	*1	Min	Max	
	Blank	50	400	Blank
	Rank Y	50	150	YE
	Rank GR	100	300	GR
TLP185	Rank GB	100	400	GB
111103	Rank YH	75	150	Y+
	Rank GRL	100	200	G
	Rank GRH	150	300	G+
	Rank BLL	200	400	В

<sup>\*1:</sup> EX, Rank GB: TLP185 (GB,E

(Note) Application, type name for certification test, please use standard product type name, i, e. TLP185(GB,E: TLP185



#### Absolute Maximum Ratings (Ta = 25°C)

	Characteristic	Symbol	Rating	Unit
	Forward current	IF	50	mA
	Forward current detating (Ta ≥ 90°C)	ΔI <sub>F</sub> / °C	-1.5	mA / °C
	Pulse forward current (Note1)	IFP	1	А
LED	Reverse voltage	V <sub>R</sub>	5	V
	Input power dissipation	P <sub>D</sub>	100	mW
	Input power dissipation derating (Ta≥90°C)	ΔΡ <sub>D</sub> /ΔΤα	-3.0	mW/°C
	Junction temperature	Tj	125	°C
	Collector-emitter voltage	V <sub>CEO</sub>	80	V
	Emitter-collector voltage	V <sub>ECO</sub>	7	V
Detector	Collector current	IC	50	mA
Dete	Collector power dissipation	PC	150	mW
	Collector power dissipation derating (Ta ≥ 25°C)	ΔP <sub>C</sub> / °C	-1.5	mW / °C
	Junction temperature	Tj	125	°C
Оре	rating temperature range	T <sub>opr</sub>	-55 to 110	°C
Stor	age temperature range	T <sub>stg</sub>	-55 to 125	°C
Lea	d soldering temperature	T <sub>sol</sub>	260 (10s)	°C
Total package power dissipation		P <sub>T</sub>	200	mVV
Tota	al package power dissipation derating (Ta ≥ 25°C)	ΔP <sub>T</sub> / °C	-2.0	mW / °C
Isola	ation voltage (AC, 1min., R.H. ≤ 60%) (Note 2)	BVS	3750	V <sub>rms</sub>

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: Pulse width ≤ 100 µs,f=100 Hz

Note 2: Device considered a two terminal device: Pins 1 and 3 shorted together and 4 and 6 shorted together.

### **Recommended Operating Conditions (Note)**

Characteristic	Symbol	Min	Тур.	Max	Unit
Supply voltage	V <sub>CC</sub>	_	5	48	V
Forward current	l <sub>F</sub>	_	16	20	mA
Collector current	IC	_	1	10	mA

Note: Recommended operating conditions are given as a design guideline to obtain expected performance of the device. Additionally, each item is an independent guideline respectively. In developing designs using this product, please confirm specified characteristics shown in this document.



## Individual Electrical Characteristics (Ta = 25°C)

	Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
	Forward voltage	V <sub>F</sub>	I <sub>F</sub> = 10 mA	1.1	1.25	1.4	V
LED	Reverse current	I <sub>R</sub>	V <sub>R</sub> = 5 V	_	_	5	μΑ
	Capacitance	C <sub>T</sub>	V = 0, f = 1 MHz	_	30	_	pF
	Collector–emitter breakdown voltage	V <sub>(BR)</sub> CEO	I <sub>C</sub> = 0.5 mA	80	-	-	٧
ctor	Emitter–collector breakdown voltage	V <sub>(BR)</sub> ECO	I <sub>E</sub> = 0.1 mA	7	_	_	V
Detector	Collector dark current	I <sub>CEO</sub>	V <sub>CE</sub> = 48 V	_	0.01	0.08	μΑ
			V <sub>CE</sub> = 48 V, Ta = 85°C	_	2	50	μA
	Capacitance (collector to emitter)	C <sub>CE</sub>	V = 0, f = 1 MHz	_	10	_	pF

## Coupled Electrical Characteristics (Ta = 25°C)

Characteristic	Symbol	Test Condition	MIn	Тур.	Max	Unit
Current transfer ratio	$I_C / I_F$ $I_F = 5 \text{ mA}, V_{CE} = 5 \text{ V}$ Rani	I <sub>F</sub> = 5 mA, V <sub>CE</sub> = 5 V	50	_	400	%
Current transfer ratio		Rank GB	100	_	400	70
Saturated CTR	IC / IF (sat)	IF = 1 mA, V <sub>CE</sub> = 0.4 V Rank GB		60		- %
Saturated CTT			30	_	1	
		I <sub>C</sub> = 2.4 mA, I <sub>F</sub> = 8 mA	1	_	0.3	
Collector–emitter saturation voltage	VCE (sat)	I <sub>C</sub> = 0.2 mA, I <sub>F</sub> = 1 mA Rank GB	1	0.2	1	V
Ç			_	_	0.3	
Off-state collector current	I <sub>C (off)</sub>	V <sub>F</sub> = 0.7V, V <sub>CE</sub> = 48 V	_	1	10	μA

## Isolation Characteristics (Ta = 25°C)

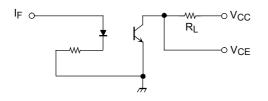
Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Capacitance (input to output)	Cs	V <sub>S</sub> = 0V, f = 1 MHz	_	0.8	_	pF
Isolation resistance	R <sub>S</sub>	V <sub>S</sub> = 500 V, R.H. ≤ 60%	1×10 <sup>12</sup>	10 <sup>14</sup>	_	Ω
	BVS	AC, 1 minute	3750	_	_	V
Isolation voltage		AC, 1 second, in oil	_	10000	_	V <sub>rms</sub>
		DC, 1 minute, in oil	_	10000		V <sub>dc</sub>

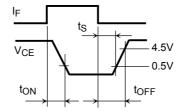


## Switching Characteristics (Ta = 25°C)

Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Rise time	t <sub>r</sub>		_	2	_	
Fall time	t <sub>f</sub>	$V_{CC} = 10 \text{ V, } I_{C} = 2 \text{ mA}$ $R_{L} = 100\Omega$	_	3	_	- µs
Turn-on time	t <sub>on</sub>	$R_L = 100\Omega$	_	3	_	
Turn-off time	t <sub>off</sub>		_	3	_	
Turn-on time	ton		_	2	_	
Storage time	t <sub>s</sub>	$R_L = 1.9 \text{ k}\Omega$ (Fig.1) $V_{CC} = 5 \text{ V}, I_F = 16 \text{ mA}$	_	25	_	μs
Turn-off time	t <sub>OFF</sub>	71	_	40	_	

Fig. 1 Switching time test circuit





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