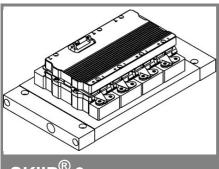
## SKiiP 2403GB172-4DW



SKiiP<sup>®</sup> 3

2-pack-integrated intelligent Power System

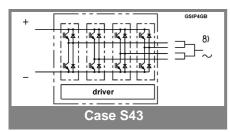
#### **Power section**

SKiiP 2403GB172-4DW

Preliminary Data

### Features

- SKiiP technology inside
- Trench IGBTs
- CAL diode technology
- Integrated current sensor
- Integrated temperature sensor
- Integrated heat sink
- IEC 60721-3-3 (humidity) class 3K3/IE32 (SKiiP<sup>®</sup> 3 System)
- IEC 60068-1 (climate) 40/125/56
- UL recognized file no. E63532
- with assembly of suitable MKP capacitor per terminal (SEMIKRON type is recommended)
- AC connection busbars must be connected by the user; copper busbars available on request

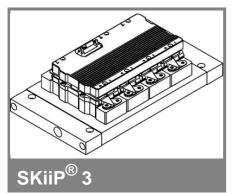


Absolute	Maximum Ratings	$T_s = 25^{\circ}C$ unless otherwise specified				
Symbol	Conditions	Values	Units			
IGBT						
V <sub>CES</sub>		1700	V			
V <sub>CC</sub> <sup>1)</sup>	Operating DC link voltage	1200	V			
V <sub>GES</sub>		±20	V			
I <sub>C</sub>	T <sub>s</sub> = 25 (70) °C	2400 (1800)	А			
Inverse diode						
I <sub>F</sub> = - I <sub>C</sub>	T <sub>s</sub> = 25 (70) °C	1800 (1400)	А			
I <sub>FSM</sub>	T <sub>j</sub> = 150 °C, t <sub>p</sub> = 10 ms; sin.	13500	A			
I²t (Diode)	Diode, T <sub>j</sub> = 150 °C, 10 ms	911	kA²s			
T <sub>i</sub> , (T <sub>stq</sub> )		- 40 + 150 (125)	°C			
V <sub>isol</sub>	rms, AC, 1 min, main terminals to heat sink	4000	V			
I <sub>AC-terminal</sub>	per AC terminal, rms, T <sub>s</sub> = 70 °C,	400	А			
	T <sub>terminal</sub> <115 °C					

Characteristics				$T_s = 25^{\circ}C$ unless otherwise specified				
Symbol Conditions				min.	typ.	max.	Units	
IGBT								
V <sub>CEsat</sub>	I <sub>C</sub> = 1200 A, measured at terr	T <sub>j</sub> = 25 <sup>minal</sup>	(125) °C;			1,9 (2,2)	2,4	V
V <sub>CEO</sub>	T <sub>i</sub> = 25 (125	) °C; at f	terminal			1 (0,9)	1,2 (1,1)	V
r <sub>CE</sub>	T <sub>i</sub> = 25 (125) °C; at terminal				0,8 (1)	1 (1,3)	mΩ	
I <sub>CES</sub>	V <sub>GE</sub> = 0 V, V T <sub>i</sub> = 25 (125	/ <sub>CE</sub> = V <sub>C</sub> ) °C	ES,			4,8 (288)		mA
E <sub>on</sub> + E <sub>off</sub>	I <sub>C</sub> = 1200 A,	$V_{CC} = 9$	900 V			780		mJ
	T <sub>j</sub> = 125 °C,	$V_{\rm CC} = 1$	200 V			1150		mJ
R <sub>CC+EE</sub> '	terminal chip, T <sub>i</sub> = 25 °C				0,13		mΩ	
L <sub>CE</sub>	top, bottom					3		nH
C <sub>CHC</sub>	per phase, A	AC-side				4		nF
Inverse o								
V <sub>F</sub> = V <sub>EC</sub>	I <sub>F</sub> = 1200 A, measured at terr	T <sub>j</sub> = 25 <sup>minal</sup>	(125) °C			2 (1,8)	2,15	V
V <sub>TO</sub>	T <sub>i</sub> = 25 (125	) °C				1,1 (0,8)	1,2 (0,9)	V
r <sub>T</sub>	T <sub>i</sub> = 25 (125					0,8 (0,8)	0,8 (0,9)	mΩ
E <sub>rr</sub>	I <sub>C</sub> = 1200 A,	$V_{CC} = 9$	900 V			144		mJ
	T <sub>j</sub> = 125 °C,	$V_{\rm CC}$ = 1	200 V			171		mJ
Mechani	cal data							
M <sub>dc</sub>	DC terminals, SI Units				6		8	Nm
$M_{ac}$	AC terminals, SI Units			13	•	15	Nm	
w	SKiiP <sup>®</sup> 3 System w/o heat sink					3,1		kg
W	heat sink					6,2		kg
	character							
SINK, I R <sub>th(j-s)l</sub>	reference		nt-m tem	perature	; 2611201 	(acc. IEC	0,013	<b>у</b> К/W
R <sub>th(j-s)D</sub>	per diode						0,015	K/W
Z <sub>th</sub>	R <sub>i</sub> (mK/W) (max. values)			tau <sub>i</sub> (s)				
-th	1 2 3 4			1	2	i <sup>(3)</sup> 3	4	
Z <sub>th(j-r)I</sub>	1,2	5	5,8	0	69	0,35	0,02	1
Z <sub>th(j-r)D</sub>	2	3	13,5	13,5	50	5	0,25	0,04
Z <sub>th(r-a)</sub>	2,7	4,6	1,1	0,6	48	15	2,8	0,4

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# SKiiP 2403GB172-4DW



### 2-pack-integrated intelligent Power System

2-pack integrated gate driver SKiiP 2403GB172-4DW

Preliminary Data

#### Gate driver features

- CMOS compatible inputs
- Wide range power supply
- Integrated circuitry to sense phase current, heat sink temperature and DC-bus voltage (option)
- Short circuit protection
- Over current protection
- Over voltage protection (option)
- Power supply protected against under voltage
- Interlick of top/bottom switch
- Isolation by transformers
- Fibre optic interface (option for GB-types only)
- IEC 60068-1 (climate) 40/85/56
- UL recognized file no. 242581

Absolute	e Maximum Ratings	T <sub>a</sub> = 25°C unless otherwise specified		
Symbol	Conditions	Values	Units	
V <sub>S2</sub>	unstabilized 24 V power supply	30	V	
V <sub>i</sub>	input signal voltage (high)	15 + 0,3	V	
dv/dt	secondary to primary side	75	kV/µs	
V <sub>isolIO</sub>	input / output (AC, rms, 2s)	4000	V	
VisoIPD	partial discharge extinction voltage, rms, $Q_{PD} \leq 10 \text{ pC}$ ;	1500	V	
V <sub>isol12</sub>	output 1 / output 2 (AC, rms, 2s)	1500	V	
f <sub>sw</sub>	switching frequency	7	kHz	
f <sub>out</sub>	output frequency for I=I <sub>C</sub> ; sin.	1	kHz	
$T_{op} (T_{stg})$	operating / storage temperature	- 40 + 85	°C	

Characte	eristics	(T <sub>a</sub> = 2			= 25°C)
Symbol	Conditions	min. typ. max.		Units	
V <sub>S2</sub>	supply voltage non stabilized	13	24	30	V
I <sub>S2</sub>	V <sub>S2</sub> = 24 V	430+45*f/kHz+0,00011*(I <sub>AC</sub> /A) <sup>2</sup>			mA
V <sub>iT+</sub>	input threshold voltage (High)			12,3	V
V <sub>iT-</sub>	input threshold voltage (Low)	4,6			V
R <sub>IN</sub>	input resistance		10		kΩ
C <sub>IN</sub>	input capacitance		1		nF
t <sub>d(on)IO</sub>	input-output turn-on propagation time		1,3		μs
t <sub>d(off)IO</sub>	input-output turn-off propagation time		1,3		μs
t <sub>pERRRESET</sub>	error memory reset time		9		μs
t <sub>TD</sub>	top / bottom switch interlock time		3,3		μs
I <sub>analogOUT</sub>	max. 5mA; 8 V corresponds to 15 V supply voltage for external components		2000		A
I <sub>s1out</sub>	max. load current			50	mA
I <sub>TRIPSC</sub>	over current trip level				
	$(I_{analog} OUT = 10 V)$		2500		А
T <sub>tp</sub>	over temperature protection	110		120	°C
U <sub>DCTRIP</sub>	U <sub>DC</sub> -protection ( U <sub>analog OUT</sub> = 9 V);	i	not mplemente	d	V
	(option for GB types)				

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