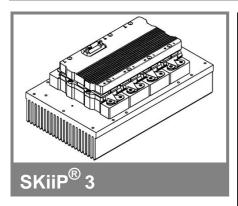
SKiiP 2013GB172-4DL



2-pack-integrated intelligent Power System

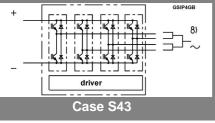
Power section SKiiP 2013GB172-4DL

Preliminary Data

Features

- SKiiP technology inside
- Trench IGBTs
- · CAL diode technology
- Integrated current sensor
- Integrated temperature sensor
- Integrated heat sink
- IEC 60721-3-3 (humidity) class 3K3/IE32 (SKiiP[®] 3 System)
- IEC 60068-1 (climate) 40/125/56
- UL recognized file no. E63532
- with assembly of suitable MKP capacitor per terminal (SEMIKRON type is recommended)
- AC connection busbars must be connected by the user; copper busbars available on request



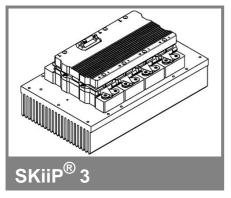


Absolute	Maximum Ratings	s = 25°C unless otherwise specified			
Symbol	Conditions	Values	Units		
IGBT					
V _{CES} V _{CC} 1)	Operating DC link voltage	1700 1200	V V		
V _{GES} I _C	T _s = 25 (70) °C	± 20 2000 (1500)	V A		
Inverse diode					
$I_F = -I_C$	$T_s = 25 (70) ^{\circ}C$	1650 (1250)	Α		
I _{FSM}	$T_j = 150 ^{\circ}\text{C}, t_p = 10 \text{ms}; \text{sin}$	13500	Α		
I ² t (Diode)	Diode, $T_j = 150 ^{\circ}\text{C}$, 10 ms	911	kA²s		
T _j , (T _{stg})		- 40 + 150 (125)	°C		
V _{isol}	rms, AC, 1 min, main terminals to heat sink	4000	V		
I _{AC-terminal}	per AC terminal, rms, T _s = 70 °C,	400	Α		
	T _{terminal} <115 °C				

Characte	Characteristics -					T _s = 25°C unless otherwise specified			
Symbol Conditions			min.	typ.	max.	Units			
IGBT									
V _{CEsat}	I _C = 1200 measured at	A, T _j = 25 terminal	(125) °C;			1,9 (2,2)	2,4	V	
V_{CEO}	$T_{j} = 25 (1)$	25) °C; at t	erminal			1 (0,9)	1,2 (1,1)	V	
r_{CE}		25) °C; at to				0,8 (1)	1 (1,3)	mΩ	
I _{CES}	$V_{GE} = 0 V_{T_i} = 25 (1)$	/, V _{CE} = V _C 25) °C	ES [,]			4,8 (288)		mA	
E _{on} + E _{off}		A, V _{CC} = 9	00 V			780		mJ	
	T _j = 125 °	°C, V _{CC} = 1	200 V			1150		mJ	
R _{CC+EE}	terminal o	chip, T _i = 25	5 °C			0,13		mΩ	
L _{CE}	top, botto	m ´				3		nΗ	
C _{CHC}	per phase	e, AC-side				6,8		nF	
Inverse o					_				
$V_F = V_{EC}$	I _F = 1200 measured at	A, T _j = 25 (terminal	(125) °C			2 (1,8)	2,15	V	
V_{TO}	T _i = 25 (1	25) °C				1,1 (0,8)	1,2 (0,9)	V	
r _T E _{rr}	$T_i' = 25 (1$					0,8 (0,8)	0,8 (0,9)	mΩ	
E _{rr}	$I_{\rm C} = 1200$	A, $V_{CC} = 9$	00 V			144		mJ	
	$T_j = 125$ °	$^{\circ}$ C, $V_{CC} = 1$	200 V			171		mJ	
Mechani	_								
M_{dc}		nals, SI Uni			6		8	Nm	
M _{ac}		nals, SI Uni			13		15	Nm	
W		System w/c	heat sink			3,1		kg	
W	heat sink					9,7		kg	
referenc	e to hea	eristics (t sink; "r	PX 16 h	eat sink nce to bu	with fan uilt-in tei	SKF16B- mperature	230-1); "s e sensor (acc.IEC	
60747-15	•				i			i	
$R_{th(j-s)l}$	per IGBT						0,015	K/W	
$R_{th(j-s)D}$	per diode					tau	0,029	K/W	
Z_{th}	R _i (mK/W) (max. values)								
_	1	2	3	4	1	2	3	4	
$Z_{\text{th(j-r)I}}$	5,6	6	6,4	0	363	0,18	0,04	1	
$Z_{\text{th(j-r)D}}$	10	8,4	14,8	14,8	50	5	0,25	0,04	
$Z_{th(r-a)}$	3,1	17,3	3,7	0,9	230	78	13	0,4	

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SKiiP 2013GB172-4DL



2-pack-integrated intelligent Power System

2-pack integrated gate driver SKiiP 2013GB172-4DL

Preliminary Data

Gate driver features

- CMOS compatible inputs
- Wide range power supply
- Integrated circuitry to sense phase current, heat sink temperature and

DC-bus voltage (option)

- Short circuit protection
- · Over current protection
- Over voltage protection (option)
- Power supply protected against under voltage
- Interlock of top/bottom switch
- Isolation by transformers
- Fibre optic interface (option for GB-types only)
- IEC 60068-1 (climate) 40/85/56
- UL recognized file no. 242581

Absolute Maximum Ratings		$T_a = 25$ °C unless otherwise specified		
Symbol	Conditions	Values	Units	
V_{S2}	unstabilized 24 V power supply	30	V	
V_{i}	input signal voltage (high)	15 + 0,3	V	
dv/dt	secondary to primary side	75	kV/μs	
$V_{\rm isollO}$	input / output (AC, rms, 2s)	4000	V	
V _{isolPD}	partial discharge extinction voltage, rms, Q _{PD} ≤10 pC;	1500	V	
V _{isol12}	output 1 / output 2 (AC, rms, 2s)	1500	V	
f _{sw}	switching frequency	7	kHz	
f_{out}	output frequency for I=I _C ; sin.	1	kHz	
$T_{op} (T_{stg})$	operating / storage temperature	- 40 + 85	°C	

Characte	eristics	$(T_a = 25^{\circ}C)$			
Symbol	Conditions	min.	typ.	max.	Units
V_{S2}	supply voltage non stabilized	13	24	30	V
I _{S2}	V _{S2} = 24 V	430+45*f/kHz+0,00011*(I _{AC} /A) ²			mA
V _{iT+}	input threshold voltage (High)			12,3	V
V_{iT-}	input threshold voltage (Low)	4,6			V
R _{IN}	input resistance		10		kΩ
C_{IN}	input capacitance		1		nF
t _{d(on)IO}	input-output turn-on propagation time		1,3		μs
$t_{d(off)IO}$	input-output turn-off propagation time		1,3		μs
$t_{pERRRESET}$	error memory reset time		9		μs
t_{TD}	top / bottom switch interlock time		3,3		μs
I _{analogOUT}	max.5mA; 8 V corresponds to 15 V supply voltage for external components		2000		Α
I _{s1out}	max. load current			50	mA
I _{TRIPSC}	over current trip level				
	$(I_{analog} OUT = 10 V)$		2500		Α
T_tp	over temperature protection	110		120	°C
UDCTRIP	U_{DC} -protection ($U_{analog OUT} = 9 V$);	i	not mplemente	d	V
	(option for GB types)				

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