

Features

- ❑ Transient protection for high-speed data lines
 - IEC 61000-4-2 (ESD) $\pm 15\text{kV}$ (Air)
 - $\pm 8\text{kV}$ (Contact)
 - IEC 61000-4-4 (EFT) 40A (5/50 ns)
 - Cable Discharge Event (CDE)
- ❑ Package optimized for high-speed lines
- ❑ Ultra-small package (1.0mm \times 0.6mm \times 0.4mm)
- ❑ Protects one data, control or power line
- ❑ Low capacitance: 0.5pF (Typical)
- ❑ Low leakage current: 0.1 μA @ V_{RWM} (Typical)
- ❑ Low clamping voltage
- ❑ Each I/O pin can withstand over 1000 ESD strikes for $\pm 8\text{kV}$ contact discharge

Description

CMESD9A05D is a low-capacitance Transient Voltage Suppressor (TVS) designed to provide electrostatic discharge (ESD) protection for high-speed data interfaces. With typical capacitance of 35pF only, CMESD9A05D is designed to protect parasitic-sensitive systems against over-voltage and over-current transient events. It complies with IEC 61000-4-2 (ESD), Level 4 ($\pm 15\text{kV}$ air, $\pm 8\text{kV}$ contact discharge), IEC 61000-4-4 (electrical fast transient - EFT) (40A, 5/50 ns), very fast charged device model (CDM) ESD and cable discharge event (CDE), etc.

Applications

- ❑ Portable Electronics
- ❑ Desktops, Servers and Notebooks
- ❑ Cellular Phones
- ❑ MP3 Ports
- ❑ Digital Camera Ports

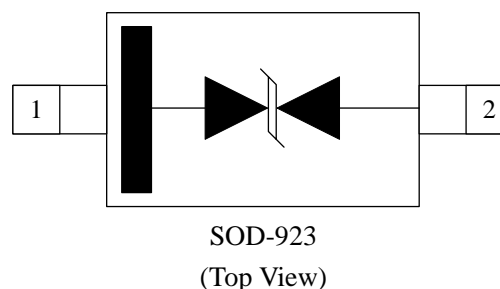
Mechanical Characteristics

- ❑ SOD-923 package
- ❑ Flammability Rating: UL 94V-0
- ❑ Marking: Part number
- ❑ Packaging: Tape and Reel

Circuit Diagram



Pin Configuration



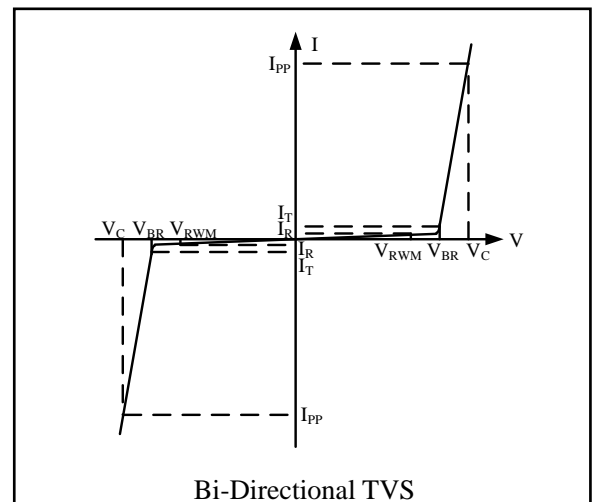


Absolute Maximum Rating

Symbol	Parameter	Value	Units
V_{ESD}	ESD per IEC 61000-4-2 (Air)	± 30	kV
	ESD per IEC 61000-4-2 (Contact)	± 30	
T_{OPT}	Operating Temperature	-55/+125	$^{\circ}C$
T_{STG}	Storage Temperature	-55/+150	$^{\circ}C$

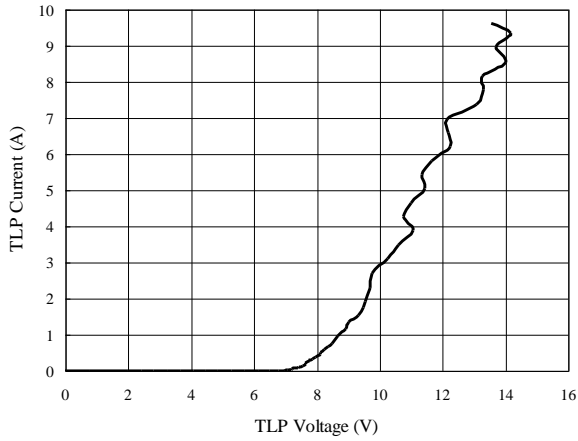
Electrical Characteristics (T = 25°C)

Symbol	Parameter
V_{RWM}	Nominal Reverse Working Voltage
I_R	Reverse Leakage Current @ V_{RWM}
V_{BR}	Reverse Breakdown Voltage @ I_T
I_T	Test Current for Reverse Breakdown
V_C	Clamping Voltage @ I_{PP}
I_{PP}	Maximum Peak Pulse Current
C_{ESD}	Parasitic Capacitance
V_R	Reverse Voltage
f	Small Signal Frequency

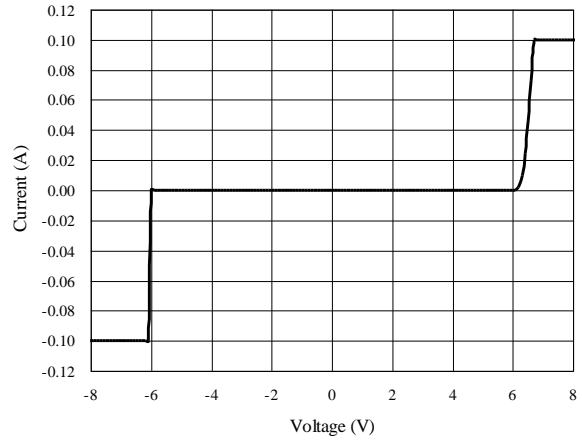


Symbol	Test Condition	Minimum	Typical	Maximum	Units
V_{RWM}				5.0	V
I_R	$V_{RWM} = 5V, T = 25^{\circ}C$ Between I/O_1 and I/O_2		0.1	1.0	μA
V_{BR}	$I_T = 1mA$ Between I/O_1 and I/O_2	5.5	6.0	8.0	V
V_C	$I_{PP} = 1A, t_p = 8/20\mu s$ Between I/O_1 and I/O_2			10	V
V_C	$I_{PP} = 4A, t_p = 8/20\mu s$ Between I/O_1 and I/O_2			15	V
C_{ESD}	$V_R = 0V, f = 1MHz$ Between I/O_1 and I/O_2		0.5	0.9	pF

TLP Measurement of I/O_1 to I/O_2

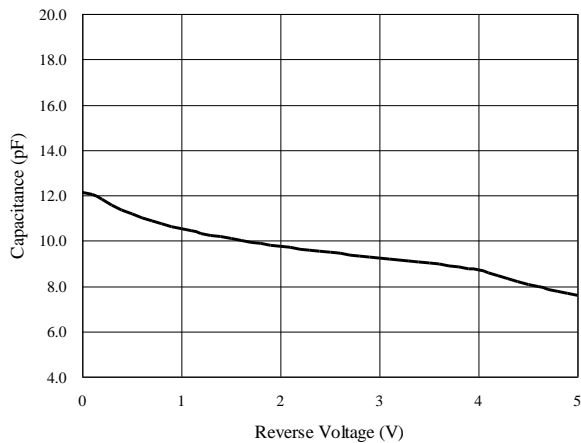


Voltage Sweeping of I/O_1 to I/O_2

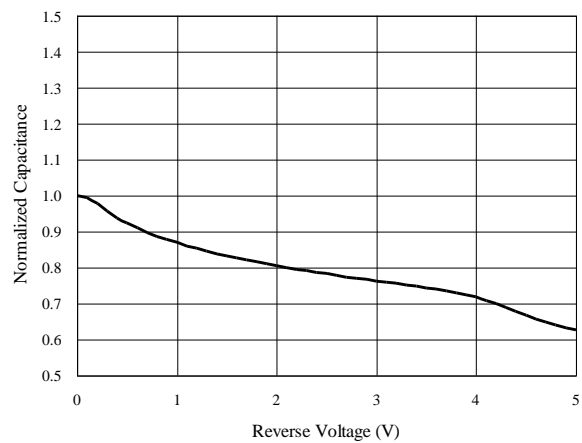


Capacitance vs. Voltage of I/O_1 to I/O_2 (f = 1MHz)

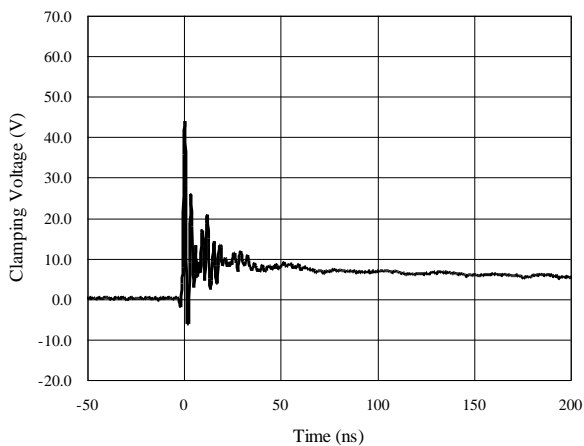
Capacitance vs. Reverse Voltage



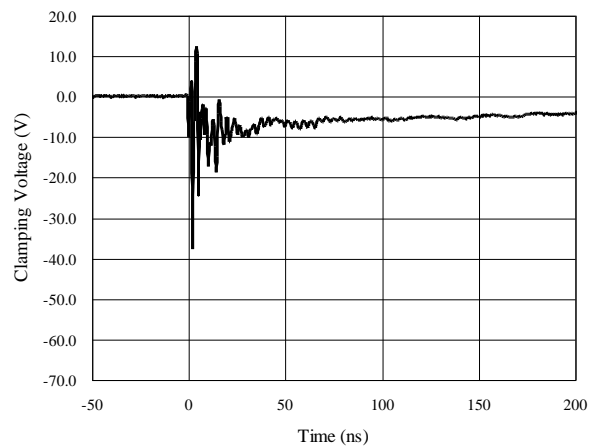
Normalized Capacitance vs. Reverse Voltage



ESD Clamping of I/O_1 to I/O_2 (+8kV Contact per IEC 61000-4-2)



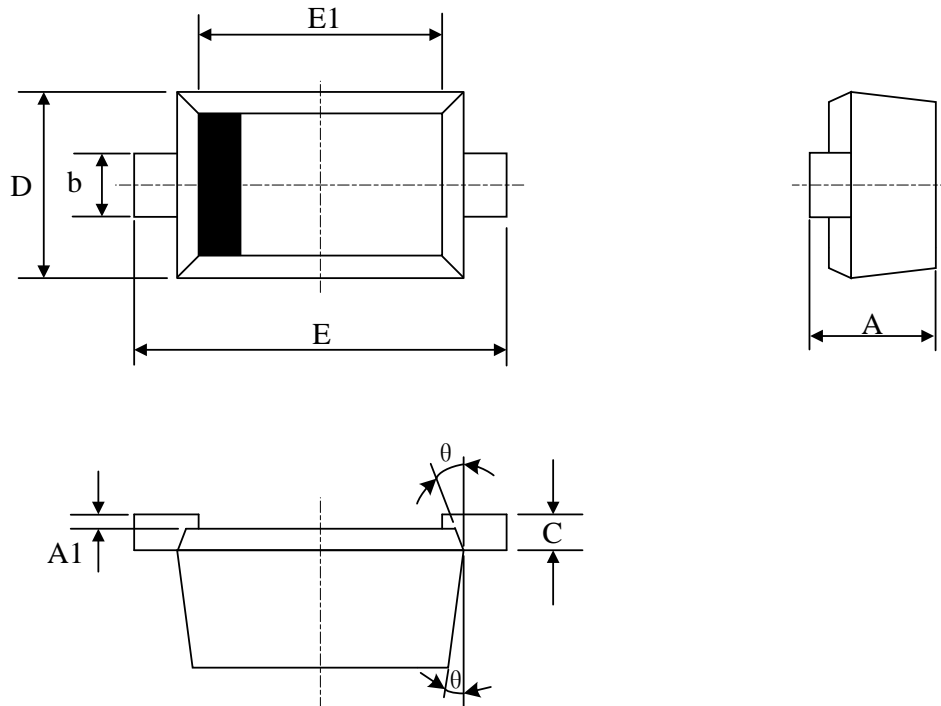
ESD Clamping of I/O_1 to I/O_2 (-8kV Contact per IEC 61000-4-2)





Package Outline

- ❑ SOD-923 package
- ❑ 2 leads, very small package
- ❑ MSL-1



Package Dimensions (Controlling dimensions are in millimeters)

Symbol	Dimensions (mm)		Dimensions (Inches)	
	Minimum	Maximum	Minimum	Maximum
A	0.350	0.430	0.014	0.017
A1	0.000	0.050	0.000	0.002
b	0.170	0.270	0.007	0.011
C		0.150		0.006
D	0.550	0.650	0.022	0.026
E	0.900	1.100	0.035	0.043
E1	0.750	0.850	0.030	0.033
θ	7° REF.		7° REF.	