

深圳市蓝光明科技有限公司 SD930 / SD940 / SD945

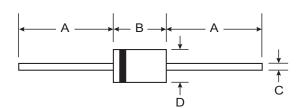
HIGH CURRENT SCHOTTKY BARRIER RECTIFIER

Features

- High Current Capability and Low Forward Drop
- High Surge Capacity
- Guard Ring for Transient Protection
- Low Power Loss, High Efficiency
- Lead Free Finish, RoHS Compliant (Note 5)

Mechanical Data

- Case: DO-201AD
- Case Material: Molded Plastic. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020C
- Terminals: Finish Bright Tin. Leads Solderable per MIL-STD-202, Method 208
- Polarity: Cathode band
- Mounting Position: Any
- Ordering Information: See Last Page
- Weight: 1.1 grams (approximate)



DO-201AD					
Dim	Min	Max			
Α	25.40	_			
В	7.20	9.50			
С	1.20	1.30			
D	4.80	5.30			
All Dimensions in mm					

Maximum Ratings and Electrical Characteristics

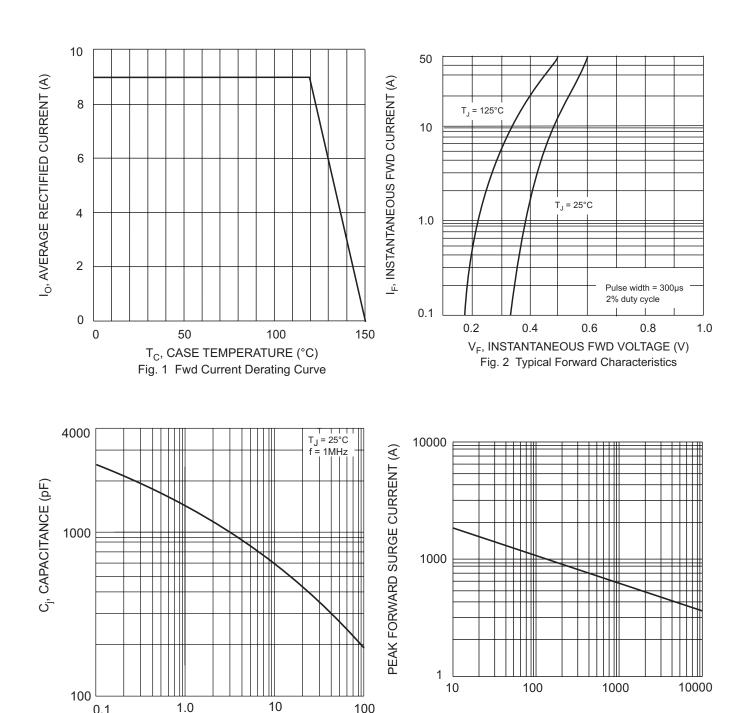
@ T_A = 25°C unless otherwise specified

Single phase, half wave, 60Hz, resistive or inductive load. For capacitive load, derate current by 20%.

Characteristic		SD930	SD940	SD945	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V _{RRM} V _{RWM} V _R	30	40	45	V
Maximum Average Forward Current @ T _C = 120°C (Note 2)		9.0			Α
Maximum Peak One-Cycle @ 5µs Sine Wave Surge Current @ 10ms Sine Wave		2150 340			А
Forward Voltage (Note 1) @ I _F = 9.0A, T _J = 25°C @ I _F = 9.0A, T _J = 125°C @ I _F = 18A, T _J = 25°C @ I _F = 18A, T _J = 125°C		0.48 0.42 0.57 0.52			V
Voltage Rate of Change		10,000			V/µs
Peak Reverse Current at Rated DC Blocking Voltage (Note 1) @T _J = 25°C @T _J = 125°C		0.8 70			mA
Maximum Junction Capacitance (Note 2)		900			pF
Typical Thermal Resistance Junction to Case (Note 4)		8.0			K/W
Operating and Storage Temperature Range		-65 to +150			°C

Notes:

- 1. Pulse width $\leq \mu s$ Duty Cycle \leq 2%.
- 2. Measured at 1.0MHz and applied reverse voltage of 4.0V.
- 3. Device mounted to heat sink with 1/8" lead length.
- 4. Thermal Resistance from Junction to Lead Vertical PC Board Mounting, 9.5mm Lead Length.
- 5. RoHS revision 13.2.2003. Glass and High Temperature Solder Exemptions Applied, see EU Directive Annex Notes 5 and 7.



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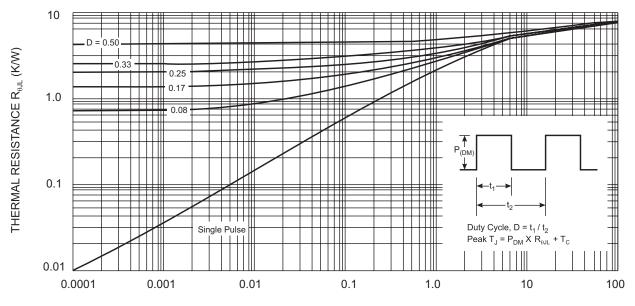
t_p, PULSE DURATION (ms)

Fig. 4, Maximum Non-repetitive Surge Current

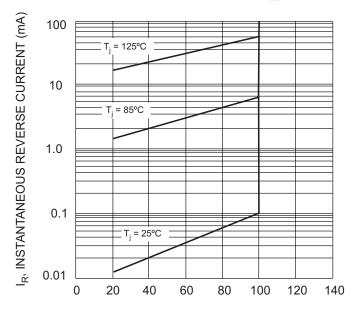
0.1

V_R, REVERSE VOLTAGE (V)

Fig. 3 Maximum Junction Capacitance



 t_1 , RECTANGULAR PULSE DURATION (seconds) Fig. 5, Typical Thermal Resistance $R_{\theta JL}$



PERCENT OF RATED PEAK REVERSE VOLTAGE (%) Fig. 6, Typical $\rm I_R$ vs. % of $\rm V_R$