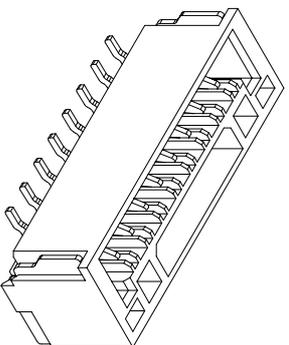
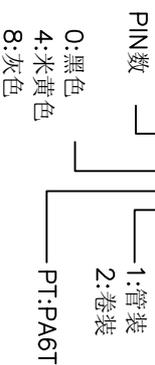


Recommended P.C.Board Layout

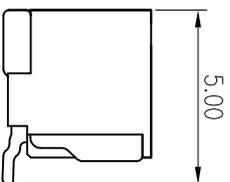
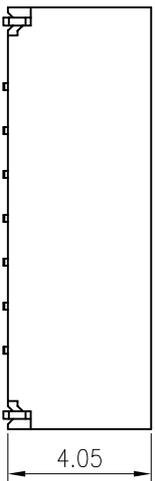
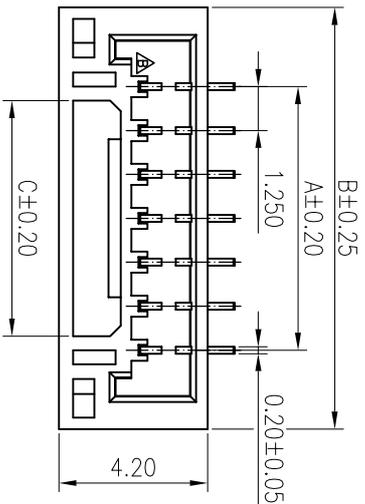


Ordering Code:

C1259VS***1***0



Part No	DIMENSIONS		
	A	B	C
C1259VS0021***0	1.25	5.75	2.20
C1259VS0031***0	2.50	7.00	3.30
C1259VS0041***0	3.75	8.25	4.55
C1259VS0051***0	5.00	9.50	4.55
C1259VS0061***0	6.25	10.75	6.55
C1259VS0071***0	7.50	12.00	6.55
C1259VS0081***0	8.75	13.25	6.55
C1259VS0091***0	10.00	14.50	6.55
C1259VS0101***0	11.25	15.75	8.55



GENERAL TOLERANCE	UNIT: mm	DATE	TITLE:
.X±0.30	X°±1°	2014.4.7	JST GHR 1.25 SMT
.XX±0.20	.X°±		180° Wafer


东莞市康瑞电子有限公司
 Dongguan A Rai Electronic Co., LTD.

DRAWN:	CHECKED:	APPROVED:	VER.	SCALE:
朱佑武		周志龙	C	5:1

PROJECT	PART NO.	DRAWING NO.
	C1259VS***1***0	1259WS01-1-S

DATE	VERSION RECORD	XXX±0.10	.XX±
2014.4.7	尺寸减小		

VER.	SHEET:
1/1	1/1

LTR	NAME	DATE
	朱佑武	2014.4.7

GENERAL TOLERANCE	UNIT: mm	DATE	TITLE:
.X±0.30	X°±1°	2014.4.7	JST GHR 1.25 SMT
.XX±0.20	.X°±		180° Wafer

PROJECT	PART NO.	DRAWING NO.
	C1259VS***1***0	1259WS01-1-S

DATE	VERSION RECORD	XXX±0.10	.XX±
2014.4.7	尺寸减小		

VER.	SHEET:
1/1	1/1

LTR	NAME	DATE
	朱佑武	2014.4.7