### 16 TO 50 VOLTS INPUT - 25 WATT

#### **FEATURES**

- 16 to 50 volt input
- Up to 87% efficiency, 42 W/in<sup>3</sup>
- · Undervoltage lockout
- · -55°C to +125°C operation
- · Fully isolated
- Magnetic feedback
- Fixed frequency, 500 kHz typical
- 65 volts for up to 1 sec. transient protection
- · Inhibit and sync function
- · Output short circuit protection



MODELS								
Оитрит V	DLTAGE (V)							
SINGLE	DUAL							
1.8	±5							
2.5	±7							
3.3	±12							
5	±15							
5.7								
12								
15								
28								

#### **DESCRIPTION**

The Interpoint® MFK Series™ of high frequency dc-dc converters offers a wide input voltage range of 16 to 50 volts and up to 25 watts of output power. The converters are capable of withstanding transients of up to 65 volts for up to one second. The package is a hermetically sealed, welded metal case. Flanged and non-flanged models are available.

### **CONVERTER DESIGN**

The MFK converters are switching regulators that use a quasisquare wave, single-ended forward converter design with a constant switching frequency of 500 kHz, typical. Isolation between input and output circuits is provided with transformers in the forward path and in the feedback control loop.

#### **HIGHER POWER DENSITY**

The MFK Series offers a new standard of performance for small size and high power density. At just 0.360 inch high and a total footprint of 1.7 in<sup>2</sup>, this low profile package offers a total power density of up to 42 watts per cubic inch.

### LOW NOISE, HIGH AUDIO REJECTION

The MFK converters current mode control system provides excellent dynamic response and noise rejection. Audio rejection is typically 50 dB. Output voltage response for a 50% to 100% step load transient is as low as 4% with a 400  $\mu$ s recovery time.

### INHIBIT FUNCTION

MFK converters provide an inhibit terminal that can be used to disable internal switching, resulting in no output and very low quiescent input current. The converter is inhibited when the inhibit pin is pulled below 0.8 volts. The converter is enabled when the pin, which is internally connected to a pull-up current source, is left unconnected or is connected to an open-collector gate. The open circuit voltage associated with the inhibit pin is 8.5 to 12 V. In the inhibit mode, a maximum of 4 mA must be sunk from the inhibit pin. See Table 5 on page 5 for more information.

### **SYNCHRONIZATION**

A synchronization feature is included with the MFK Series that allows the user to match the switching frequency of the converter to the frequency of a system clock. Synchronization allows the user to adjust the nominal 500 kHz operating frequency to any frequency within the range of 450 kHz to 550 kHz. This is initiated by applying an active high input of the desired frequency to the sync pin. See Table 5 on page 5 for more information.

#### **SHORT CIRCUIT PROTECTION**

MFK Series converters provide short circuit protection by restricting the output current to approximately 115% of the full load output current.

### UNDERVOLTAGE LOCKOUT

Undervoltage lockout with hysteresis prevents the converters from operating below approximately 15 volts input voltage to keep system current levels smooth, especially during initialization or re-start operations.



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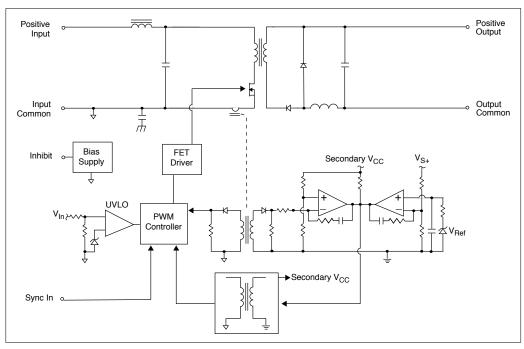


FIGURE 1: MFK SINGLE BLOCK DIAGRAM

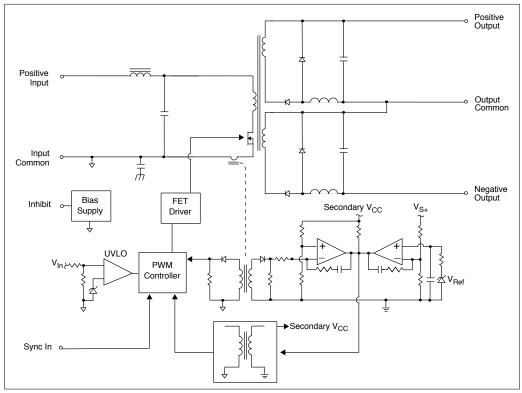


FIGURE 2: MFK DUAL BLOCK DIAGRAM

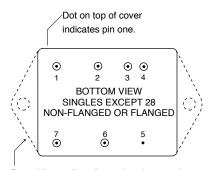
### **16 TO 50 VOLTS INPUT - 25 WATT**

	PIN OUT									
Pin	Single Output	Dual Output								
1	Inhibit	Inhibit	Inhibit							
2	Output Common	Positive Output	Positive Output							
3	Positive Output	No Connection	Output Common							
4	Sync In	Output Common	Negative Output							
5	Case Ground	Sync In	Sync In							
6	Input Common	Case Ground	Case Ground							
7	Positive Input	Input Common	Input Common							
8	_	Positive Input	Positive Input							

TABLE 1: PIN OUT

PINS NOT IN USE						
Inhibit	Leave unconnected					
Sync	Leave unconnected					

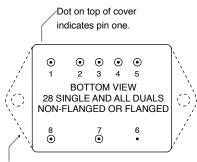
TABLE 2: PINS NOT IN USE



Dotted line outlines flanged package option.

See Figure 26 on page 16 and Figure 28 on page 18 for dimensions.

FIGURE 3: MFK SINGLE PIN OUT (EXCEPT 28S)



Dotted line outlines flanged package option.

See Figure 27 on page 17 and Figure 29 on page 19 for dimensions.

FIGURE 4: MFK DUAL PIN OUT (INCLUDES 28S)

### 16 TO 50 VOLTS INPUT - 25 WATT

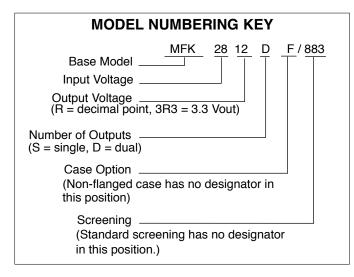


FIGURE 5: MODEL NUMBERING KEY

SMD NUMBERS							
STANDARD MICROCIRCUIT DRAWING (SMD)	MFK SIMILAR PART						
5962-1421001HXC IN PROCESS	MFK281R8S/883						
5962-1421002HXC IN PROCESS	MFK282R5S/883						
5962-1421003HXC IN PROCESS	MFK283R3S/883						
5962-1421004HXC IN PROCESS	MFK2805S/883						
5962-1421005HXC IN PROCESS	MFK285R7S/883						
5962-1421006HXC IN PROCESS	MFK2812S/883						
5962-1421007HXC IN PROCESS	MFK2815S/883						
5962-1421008HTC IN PROCESS	MFK2828S/883						
5962-1421101HXC IN PROCESS	MFK2805D/883						
5962-1421102HXC IN PROCESS	MFK2807D/883						
5962-1421103HXC IN PROCESS	MFK2812D/883						
5962-1421104HXC IN PROCESS	MFK2815D/883						

The SMD number shown is for Class H screening, non-flanged. To indicate the flanged case option change the "X" to "Z" In the SMD number ("T" to "U" for 28 single). For exact specifications for an SMD product, refer to the SMD drawing. SMDs can be downloaded from: http://www.landandmaritime.dla.mil/programs/smcr

TABLE 3: SMD CROSS REFERENCE

#### **MODEL NUMBER OPTIONS** TO DETERMINE THE MODEL NUMBER ENTER ONE OPTION FROM EACH CATEGORY IN THE FORM BELOW. Base Model and Case Options 3 Screening 4 Output Voltage 1 Number of **CATEGORY** Input Voltage Outputs 2 1R8, 2R5, 3R3, S (non-flanged, leave blank) (standard, leave blank) 05, 5R7, 12, 15, 28 **OPTIONS** MFK28 05, 07, 12, 15 F (flanged) ES 883 **FILL IN FOR** MFK28 MODEL#

- 1. Output Voltage: An R indicates a decimal point. 1R8 is 1.8 volts out. The values of 1.8, 2.5, 3.3 and 5.7 volts are only available in single output models.
- 2. Number of Outputs: S is a single output and D is a dual output.
- 3. Case Options: For the standard case, Figure 26 on page 16 or Figure 27 on page 17, leave the case option blank. For the flanged case option, Figure 28 on page 18 or Figure 29 on page 19, insert the letter F in the Case Option position.
- 4. Screening: For standard screening leave the screening option blank. For other screening options, insert the desired screening level. For more information see Table 11 on page 20 and Table 12 on page 21.

TABLE 4: MODEL NUMBER OPTIONS

### **16 TO 50 VOLTS INPUT - 25 WATT**

Table 5: Operating Conditions, All Models, 25°C case, 28 Vin, 100% load, unless otherwise specified.

MFK SERIES		AL	L MODE	ELS	
PARAMETER	CONDITIONS	MIN	TYP	MAX	UNITS
LEAD SOLDERING TEMPERATURE <sup>1</sup>	10 SECONDS MAX.	_	_	300	°C
STORAGE TEMPERATURE <sup>1</sup>		-65	_	+150	°C
CASE OPERATING	FULL POWER	-55	_	+125	°C
TEMPERATURE	ABSOLUTE <sup>1</sup>	-55	_	+135	
DERATING OUTPUT POWER/CURRENT <sup>1</sup>	LINEARLY	From 1	00% at 1	25°C to 0	0% at 135°C
ESD RATING <sup>1</sup>	MIL-STD-883, METHOD 3015	2	000 - 399	19	V
MIL-PRF-38534, 3.9.5.8.2	CLASS 2	_	000 000	.0	•
ISOLATION: INPUT TO OUTPUT OR ANY	500 VDC AT 25°C	100	_		Megohms
PIN TO CASE EXCEPT CASE PIN	000 100 111 20 0	100			Wiegeriine
UNDERVOLTAGE LOCKOUT		_	15	_	V
CURRENT LIMIT <sup>2</sup>	% OF FULL LOAD	_	115	_	%
AUDIO REJECTION <sup>1</sup>		_	50	_	dB
CONVERSION FREQUENCY	FREE RUN -55°C TO +125°C	430	_	570	kHz
SYNCHRONIZATION	INPUT FREQUENCY	450	_	550	kHz
	DUTY CYCLE <sup>1</sup>	40	_	60	%
	ACTIVE LOW	_	_	0.8	V
	ACTIVE HIGH <sup>1</sup>	4.5	_	5.0	•
	REFERENCED TO		INPUT	СОММО	ON
	IF NOT USED	L	EAVE U	NCONNE	CTED
INHIBIT ACTIVE LOW (OUTPUT DISABLED)	INHIBIT PIN PULLED LOW 3	_	_	0.8	V
Do not apply a voltage to the inhibit pin <sup>4</sup>	INHIBIT PIN SOURCE		_	4	mA
	CURRENT <sup>1</sup>			_	11171
	REFERENCED TO		INPUT	СОММО	ON
INHIBIT ACTIVE HIGH (OUTPUT ENABLED)	INHIBIT PIN CONDITION	(	OPEN CC	DLLECTO	R OR
Do not apply a voltage to the inhibit pin <sup>4</sup>			UNCC	NNECTE	D
	OPEN PIN VOLTAGE <sup>1</sup>	8.5		12	V

For mean time between failures (MTBF) contact Applications Engineering powerapps@crane-eg.com +1-425-882-3100 option 7

- 1. Guaranteed by qualification test and/or analysis. Not an in-line test.
- Dual outputs: The over-current limit will trigger when the sum of the currents from both outputs reaches 115% (typical value) of the maximum rated "total" current of both outputs.
- 3. Tested with inhibit pin pulled to ground.
- An external inhibit interface should be used to pull the inhibit low or leave it floating. The inhibit pin can be left unconnected if not used.

### **16 TO 50 VOLTS INPUT - 25 WATT**

Table 6: Electrical Characteristics -55°C to +125°C case, 28 Vin, 100% load, free run, unless otherwise specified.

SINGLE OUTPUT MODELS		М	FK281R	8S	М	FK282R	5S	М	FK283R	3S	
PARAMETER	CONDITIONS	MIN	TYP	MAX	MIN	TYP	MAX	MIN	TYP	MAX	UNITS
OUTPUT VOLTAGE		1.74	1.80	1.86	2.42	2.50	2.58	3.20	3.30	3.40	V
OUTPUT CURRENT <sup>2</sup>	V <sub>IN</sub> = 16 TO 50	_	_	5.56	_	_	5.0	_	_	4.55	Α
OUTPUT POWER <sup>2</sup>	V <sub>IN</sub> = 16 TO 50	_	_	10	_	_	12.5	_	_	15	W
OUTPUT RIPPLE	T <sub>C</sub> = 25°C	_	30	60	_	30	60	_	25	60	mV p-p
10 kHz - 20 MHz	T <sub>C</sub> = -55°C TO +125°C	_	35	80	_	35	80	_	30	80	, , ,
LINE REGULATION	V <sub>IN</sub> = 16 TO 50	_	5	20	_	5	20	_	5	20	mV
LOAD REGULATION	NO LOAD TO FULL	_	10	25	_	5	25	_	5	25	mV
INPUT VOLTAGE	CONTINUOUS	16	28	50	16	28	50	16	28	50	V
NO LOAD TO FULL	TRANSIENT 1 sec.1	_	_	65	_	_	65	_	_	65	V
INPUT CURRENT	NO LOAD	_	25	50	_	25	50	_	25	50	mA
	INHIBITED	_	2.5	4	_	2.5	4	_	2.5	4	
INPUT RIPPLE CURRENT	10 kHz - 20 MHz	_	30	75	_	30	80	_	30	80	mA p-p
EFFICIENCY	TC = 25°C	69	72	_	73	76	_	76	79	_	- %
	TC = -55°C TO +125°C	67	_	_	71	_	_	74	_	_	7
LOAD FAULT 3, 4	POWER DISSIPATION	_	_	8.5	_	_	8.5	_	_	8.5	W
SHORT CIRCUIT	RECOVERY 1	_	_	20	_	_	20	_	_	20	ms
STEP LOAD RESPONSE 5, 6	TRANSIENT	_	125	200	_	125	200	_	125	200	mV pk
50% - 100% - 50%	RECOVERY	_	200	400	_	100	300	_	200	300	μs
STEP LINE RESPONSE 1, 5, 6	TRANSIENT	_	_	350	_	_	350	_	_	350	mV pk
16 - 50 - 16 V	RECOVERY	_	0.5	1	_	0.5	1	_	0.5	1	ms
START-UP <sup>7</sup>	DELAY	_	_	25	_	_	25	_	_	25	ms
FULL LOAD	OVERSHOOT 1	_	_	50	_	_	50	_	_	50	mV pk
CAPACITIVE LOAD 1	NO EFFECT ON DC	_	_	2000	_	_	2000	_	_	2000	μF
$T_C = 25^{\circ}C$	PERFORMANCE			2000			2000			2000	"

- 1. Guaranteed by qualification test and/or analysis. Not an in-line test.
- 2. A minimum load of 20% is recommended.
- 3. Short circuit measured with 1% 10 milliohm resistive load.4. Indefinite short circuit protection not guaranteed above 125°C.
- 5. Step transition time >10  $\mu$ s.
- 6. Recovery and startup times are measured from application of the transient or change in condition to the point at which  $V_{OUT}$  is within 1% of final value.  $C_L = 0$ .
- 7. Tested on release from inhibit.

### **16 TO 50 VOLTS INPUT - 25 WATT**

Table 7: Electrical Characteristics -55°C to +125°C case, 28 Vin, 100% load, free run, unless otherwise specified.

SINGLE OUTPUT MODELS		l M	1FK2805	is	МІ	FK285R	7S	M	IFK2812	!S	
PARAMETER	CONDITIONS	MIN	TYP	MAX	MIN	TYP	MAX	MIN	TYP	MAX	UNITS
OUTPUT VOLTAGE		4.85	5.00	5.15	5.52	5.70	5.87	11.76	12.00	12.24	V
OUTPUT CURRENT <sup>2</sup>	V <sub>IN</sub> = 16 TO 50	_	_	4.0	_	_	4.0	_	_	2.08	Α
OUTPUT POWER 2	V <sub>IN</sub> = 16 TO 50	_	_	20	-	_	22.8	_	_	25	W
OUTPUT RIPPLE	T <sub>C</sub> = 25°C	_	40	80	_	40	80	_	35	80	mV p-p
10 kHz - 20 MHz	T <sub>C</sub> = -55°C TO +125°C	_	_	100	_	_	100	_	_	100	
LINE REGULATION	V <sub>IN</sub> = 16 TO 50	_	5	20	_	5	20	_	5	20	mV
LOAD REGULATION	NO LOAD TO FULL	_	5	25	_	5	25	_	5	20	mV
INPUT VOLTAGE	CONTINUOUS	16	28	50	16	28	50	16	28	50	V
NO LOAD TO FULL	TRANSIENT 1 sec.1	_	_	65	_	_	65	_	_	65	V
INPUT CURRENT	NO LOAD	_	25	50	_	25	50	_	20	50	mA
	INHIBITED	_	2.4	4	-	2.4	4	_	2.5	4	
INPUT RIPPLE CURRENT	10 kHz - 20 MHz	_	40	85	_	40	85	_	45	90	mA p-p
EFFICIENCY	T <sub>C</sub> = 25°C	79	82	_	79	82	_	83	86	_	%
	T <sub>C</sub> = -55°C TO +125°C	77	_	_	77	_	_	81	_	_	
LOAD FAULT 3, 4	POWER DISSIPATION	_	6	8.5	_	6	8.5	_	6	8.5	W
SHORT CIRCUIT	RECOVERY 1	_	_	20	_	_	20	_	_	20	ms
STEP LOAD RESPONSE 5, 6	TRANSIENT	_	_	400	_	_	400	_	350	500	mV pk
50% - 100% - 50%	RECOVERY	_	_	300	_	_	300	_	300	500	μs
STEP LINE RESPONSE 1, 5, 6	TRANSIENT	_	_	500	_	_	570	_	_	1300	mV pk
16 - 50 - 16 V	RECOVERY	_	0.5	1	_	0.5	1	_	0.5	1	ms
START-UP <sup>7</sup>	DELAY	_	_	25	_	_	25	_	_	25	ms
FULL LOAD	OVERSHOOT 1	_	0	50	_	0	50	_	0	120	mV pk
CAPACITIVE LOAD 1	NO EFFECT ON DC	_	_	2000	_	_	2000	_	_	2000	μF
$T_C = 25^{\circ}C$	PERFORMANCE			2000			2000			2000	"

- 1. Guaranteed by qualification test and/or analysis. Not an in-line test.
- 2. A minimum load of 20% is recommended.
- 3. Short circuit measured with 1% 10 milliohm resistive load.
- 4. Indefinite short circuit protection not guaranteed above 125°C
- 5. Step transition time >10  $\mu$ s.
- 6. Recovery and startup times are measured from application of the transient or change in condition to the point at which V<sub>OUT</sub> is within 1% of final value. C<sub>L</sub> = 0.
- 7. Tested on release from inhibit.

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TABLE 8: ELECTRICAL CHARACTERISTICS -55°C TO +125°C CASE, 28 VIN, 100% LOAD, FREE RUN, UNLESS OTHERWISE SPECIFIED.

SINGLE OUTPUT MODELS		M	1FK2815	is	M	IFK2828	S	
PARAMETER	CONDITIONS	MIN	TYP	MAX	MIN	TYP	MAX	UNITS
OUTPUT VOLTAGE		14.70	15.00	15.30	27.16	28.00	28.84	V
OUTPUT CURRENT <sup>2</sup>	V <sub>IN</sub> = 16 TO 50	_	_	1.67	_	_	0.89	Α
OUTPUT POWER <sup>2</sup>	V <sub>IN</sub> = 16 TO 50	_	_	25	-	_	25	W
OUTPUT RIPPLE	T <sub>C</sub> = 25°C	_	40	80	_	80	150	mV p-p
10 kHz - 20 MHz	T <sub>C</sub> = -55°C TO +125°C	_	50	100	-	100	200	
LINE REGULATION	V <sub>IN</sub> = 16 TO 50	_	5	20	_	150	280	mV
LOAD REGULATION	NO LOAD TO FULL	_	5	20	_	150	280	mV
INPUT VOLTAGE	CONTINUOUS	16	28	50	16	28	50	V
NO LOAD TO FULL	TRANSIENT 1 sec. <sup>1</sup>	_	_	65	_	_	65	V
INPUT CURRENT	NO LOAD	_	20	50	_	30	55	mA
	INHIBITED	_	2.5	4	_	2.5	4	1117
INPUT RIPPLE CURRENT	10 kHz - 20 MHz	_	50	110	_	50	100	mA p-p
EFFICIENCY	T <sub>C</sub> = 25°C	84	87	_	81	84	_	%
	T <sub>C</sub> = -55°C TO +125°C	82	_	_	80	_	_	,,
LOAD FAULT 3, 4	POWER DISSIPATION	_	6	8	_	6	8.5	W
SHORT CIRCUIT	RECOVERY 1	_	_	20	-	_	20	ms
STEP LOAD RESPONSE 5, 6	TRANSIENT	_	400	600	_	900	1200	mV pk
50% - 100% - 50%	RECOVERY	_	300	500	_	500	600	μs
STEP LINE RESPONSE 1, 5, 6	TRANSIENT	_	_	1500	_	_	2800	mV pk
16 - 50 - 16 V	RECOVERY	_	0.5	1	_	0.5	1	ms
START-UP <sup>7</sup>	DELAY	_	_	25	_	_	25	ms
FULL LOAD	OVERSHOOT 1	_	0	150	_	0	280	mV pk
CAPACITIVE LOAD 1	NO EFFECT ON DC	_	_	2000	_	_	1000	μF
$T_C = 25^{\circ}C$	PERFORMANCE			2000			1000	μ'

- 1. Guaranteed by qualification test and/or analysis. Not an in-line test.
- 2. A minimum load of 20% is recommended.
- 3. Short circuit measured with 1% 10 milliohm resistive load.
- 4. Indefinite short circuit protection not guaranteed above 125°C
- 5. Step transition time >10  $\mu$ s.
- 6. Recovery and startup times are measured from application of the transient or change in condition to the point at which  $V_{OUT}$  is within 1% of final value.  $C_L = 0$ . 7. Tested on release from inhibit.

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Table 9: Electrical Characteristics -55°C to +125°C case, 28 Vin, 100% load, free run, unless otherwise specified.

DUAL OUTPUT MODELS		M	IFK2805	SD.	l M	MFK2807D			
PARAMETER	CONDITIONS	MIN	TYP	MAX	MIN	TYP	MAX	UNITS	
OUTPUT VOLTAGE	+ V <sub>OUT</sub>	4.85	5.00	5.15	6.86	7.00	7.14	V	
	- V <sub>OUT</sub>	4.82	5.00	5.18	6.83	7.00	7.17		
OUTPUT CURRENT <sup>2, 3</sup>	EITHER OUTPUT	_	±2.0	2.80	_	±1.5	2.10	Α	
V <sub>IN</sub> = 16 TO 50 V	TOTAL OUTPUT	_	_	4.0	_	_	3.0		
OUTPUT POWER <sup>2, 3</sup>	EITHER OUTPUT	_	±10	18	_	±10.5	18.9	w	
V <sub>IN</sub> = 16 TO 50 V	TOTAL OUTPUT	_	_	20	_	_	21		
OUTPUT RIPPLE	T <sub>C</sub> = 25°C	_	_	80	_	_	70	mV p-p	
±V <sub>OUT</sub> , 10 kHz - 20 MHz	$T_{C} = -55^{\circ}C \text{ TO } +125^{\circ}C$	_	_	90	_	_	80		
LINE REGULATION	+ V <sub>OUT</sub>	_	5	20	_	5	20	mV	
V <sub>IN</sub> = 16 TO 50 V	- V <sub>OUT</sub>	_	20	100	_	20	100		
LOAD REGULATION	+ V <sub>OUT</sub>	_	5	20	_	5	20	mV	
NL TO FULL, BALANCED	- V <sub>OUT</sub>	_	35	250	_	50	250		
CROSS REGULATION <sup>4</sup>	T <sub>C</sub> = 25°C	_	_	360	_	_	400	mV	
INPUT VOLTAGE	CONTINUOUS	16	28	50	16	28	50	V	
	TRANSIENT 1 sec. <sup>1</sup>	_	_	65	_	_	65	V	
INPUT CURRENT	NO LOAD	_	30	50	_	30	50	mA.	
	INHIBITED	_	2.5	4	_	2.5	4		
INPUT RIPPLE CURRENT	10 kHz - 20 MHz	_	35	70	_	35	70	mA p-p	
EFFICIENCY	T <sub>C</sub> = 25°C	78	81	_	81	83	_	%	
	$T_{\rm C}$ = -55°C TO +125°C	76	_	_	79	_	_	, -	
LOAD FAULT 5, 6	POWER DISSIPATION	_	6	8.5	_	6	8	W	
SHORT CIRCUIT	RECOVERY <sup>1</sup>	_	15	20	_	15	20	ms	
STEP LOAD RESPONSE 7, 8, 9	TRANSIENT ±V <sub>OUT</sub>	_	100	450	_	125	500	mV pk	
50%-100%-50%, BALANCED LOADS	RECOVERY	_	200	500	_	200	500	μs	
STEP LINE RESPONSE 1, 7, 9	TRANSIENT	_	_	500	_	_	700	mV pk	
±V <sub>OUT</sub> , V <sub>IN</sub> = 16 - 50 - 16 V	RECOVERY	_	0.5	1	_	0.5	1	ms	
START-UP <sup>10</sup>	DELAY	_	_	20	_	_	20	ms	
	OVERSHOOT <sup>1</sup>	_	0	50	_	0	70	mV pk	
CAPACITIVE LOAD $^{1, 11}$ $T_C = 25^{\circ}C$	NO EFFECT ON DC PERFORMANCE	_	_	1000	_	_	1000	μF	

- 1. Guaranteed by qualification test and/or analysis. Not an in-line test.
- 2. Up to 70% of the total output power is available from either output providing the opposite output is simultaneously carrying 30% of the total output power.
- 3. Recommended minimum load is 20% total load (sum of both outputs).
- 4. Effect on -Vout for the following conditions:
- +Po = 30% to 70%; -Po = 70% to 30%
- 5. Short circuit measured with 1% 10 milliohm resistive load.
- 6. Indefinite short circuit protection not guaranteed above 125°C (case)
- 7. Recovery and startup times are measured from application of the transient or change in condition to the point at which  $V_{OUT}$  is within 1% of final value.  $C_1 = 0$ .
- Response of either output with the opposite output held at half of the total output power.
- 9. Step transition time >10  $\mu$ s.
- 10. Tested on release from inhibit.
- 11. Each output.

### 16 TO 50 VOLTS INPUT - 25 WATT

Table 10: Electrical Characteristics -55°C to +125°C case, 28 Vin, 100% load, free run, unless otherwise specified.

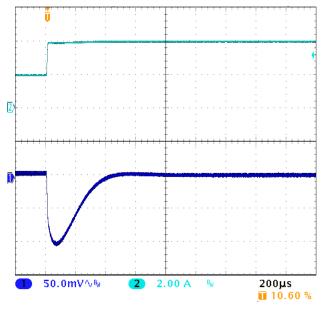
DUAL OUTPUT MODELS		N	IFK2812	D	N	/JFK2815	D	LINUTO
PARAMETER	CONDITIONS	MIN	TYP	MAX	MIN	TYP	MAX	UNITS
OUTPUT VOLTAGE	+ V <sub>OUT</sub>	11.76	12.00	12.24	14.70	15.00	15.30	V
	- V <sub>OUT</sub>	11.70	12.00	12.30	14.63	15.00	15.38	
OUTPUT CURRENT <sup>2, 3</sup>	EITHER OUTPUT	_	±1.04	1.45	_	±0.833	1.16	Α
V <sub>IN</sub> = 16 TO 50 V	TOTAL OUTPUT	_	_	2.08	_	_	1.66	
OUTPUT POWER <sup>2, 3</sup>	EITHER OUTPUT	_	±12.5	22.5	_	±12.5	22.5	W
V <sub>IN</sub> = 16 TO 50 V	TOTAL OUTPUT	_	_	25	_	_	25	
OUTPUT RIPPLE	T <sub>C</sub> = 25°C	_	_	90	_	_	90	mV p-p
±V <sub>OUT</sub> , 10 kHz - 20 MHz	T <sub>C</sub> = -55°C TO +125°C	_	_	90	_	_	90	
LINE REGULATION	+ V <sub>OUT</sub>	_	5	20	_	5	20	mV
V <sub>IN</sub> = 16 TO 50 V	- V <sub>OUT</sub>	_	20	150	_	20	150	
LOAD REGULATION	+ V <sub>OUT</sub>	-	5	20	_	5	20	mV
NL TO FULL, BALANCED	- V <sub>OUT</sub>	_	60	250	_	100	250	1111
CROSS REGULATION <sup>4</sup>	T <sub>C</sub> = 25°C	_	_	700	_	_	800	mV
INPUT VOLTAGE	CONTINUOUS	16	28	50	16	28	50	V
	TRANSIENT 1 sec.1	_	_	65	_	_	65	٧
INPUT CURRENT	NO LOAD	_	30	50	_	30	50	mA
	INHIBITED	_	2.5	4	_	2.5	4	1117
INPUT RIPPLE CURRENT	10 kHz - 20 MHz	-	50	100	_	40	100	mA p-p
EFFICIENCY	T <sub>C</sub> = 25°C	82	85	_	83	86	_	%
	T <sub>C</sub> = -55°C TO +125°C	80	_	_	81	_	_	,,,
LOAD FAULT <sup>5, 6</sup>	POWER DISSIPATION	_	5	8	_	5	8	W
SHORT CIRCUIT	RECOVERY 1	_	15	20	_	15	20	ms
STEP LOAD RESPONSE 7, 8, 9	TRANSIENT ±V <sub>OUT</sub>	_	350	600	_	400	650	mV pk
50%-100%-50%, BALANCED LOADS	RECOVERY	_	250	550	_	250	550	μs
STEP LINE RESPONSE 1, 7, 9	TRANSIENT	_	_	1300	_	_	1500	mV pk
±V <sub>OUT</sub> , V <sub>IN</sub> = 16 - 50 - 16 V	RECOVERY	_	0.5	1	_	0.5	1	ms
START-UP <sup>10</sup>	DELAY	_	_	20	_	_	20	ms
	OVERSHOOT 1	_	0	120	_	0	150	mV pk
CAPACITIVE LOAD $^{1, 11}$ $T_C = 25$ °C	NO EFFECT ON DC PERFORMANCE	_	_	1000	_	_	1000	μF

#### Note:

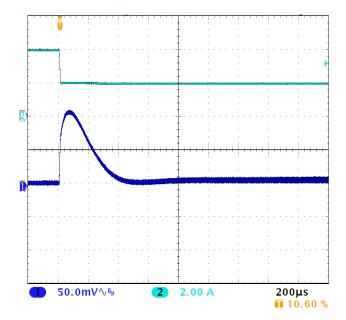
- 1. Guaranteed by qualification test and/or analysis. Not an in-line test.
- Up to 70% of the total output power is available from either output providing the opposite output is simultaneously carrying 30% of the total output power.
- 3. Recommended minimum load is 20% total load (sum of both outputs).
- 4. Effect on –Vout for the following conditions: +Po = 30% to 70%; Po = 70% to 30%
- 5. Short circuit measured with 1% 10 milliohm resistive load.
- 6. Indefinite short circuit protection not guaranteed above 125°C (case)
- Recovery and startup times are measured from application of the transient or change in condition to the point at which V<sub>OUT</sub> is within 1% of final value. C<sub>1</sub> = 0.
- C<sub>L</sub> = 0.
   Response of either output with the opposite output held at half of the total output power.
- 9. Step transition time >10  $\mu$ s.
- 10. Tested on release from inhibit.
- 11. Each output.

### 16 TO 50 VOLTS INPUT - 25 WATT

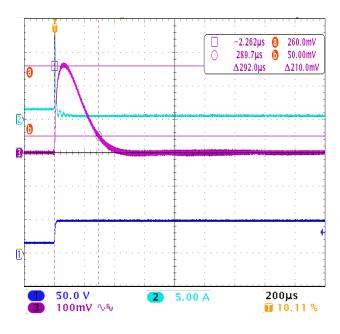
Typical Performance Plots: 25°C case, 28 Vin, 100% load, free run, unless otherwise specified. For reference only, not guaranteed specifications.



MFK2805S STEP LOAD 100% - 50% FIGURE 6

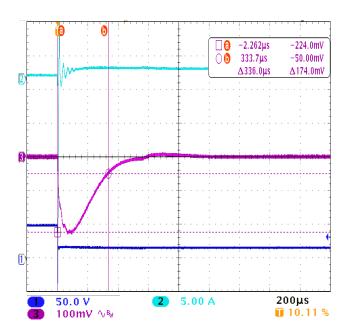


MFK2805S STEP LOAD 50% - 100% FIGURE 7



MFK2805S STEP LINE 16 - 50 VOLTS

FIGURE 8

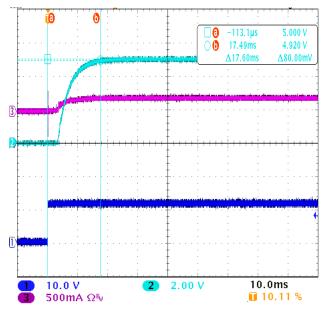


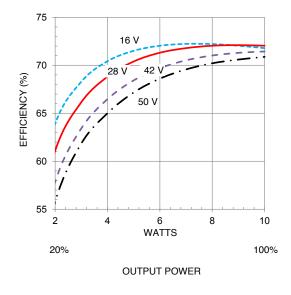
MFK2805S STEP LINE 50 - 16 VOLTS

FIGURE 9

### 16 TO 50 VOLTS INPUT - 25 WATT

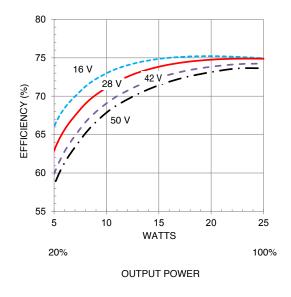
Typical Performance Plots: 25°C case, 28 Vin, 100% load, free run, unless otherwise specified. For reference only, not guaranteed specifications.

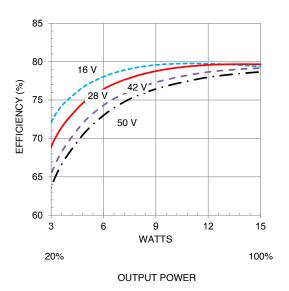




MFK2805S START-UP 20% LOAD FIGURE 10

MFK281R8S EFFICIENCY
FIGURE 11

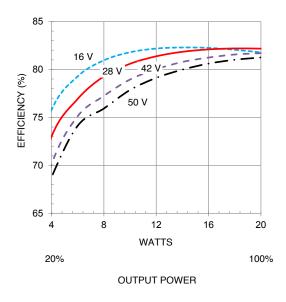


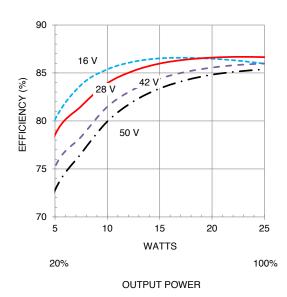


MFK282R5S EFFICIENCY FIGURE 12 MFK283R3S EFFICIENCY FIGURE 13

### 16 TO 50 VOLTS INPUT - 25 WATT

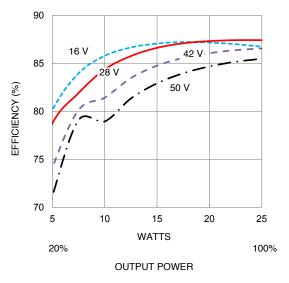
Typical Performance Plots: 25°C case, 28 Vin, 100% load, free run, unless otherwise specified. For reference only, not guaranteed specifications.



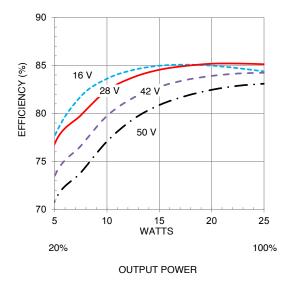


MFK2805S EFFICIENCY FIGURE 14 MFK2812S EFFICIENCY FIGURE 15

MFK285R7S Efficiency will be added in the future.



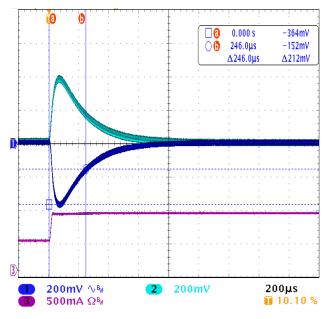




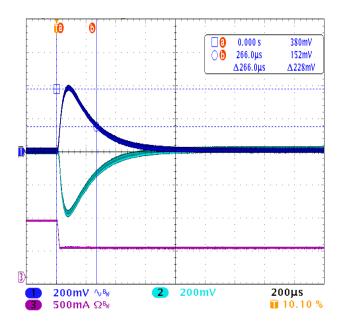
MFK2828S EFFICIENCY
FIGURE 17

### 16 TO 50 VOLTS INPUT - 25 WATT

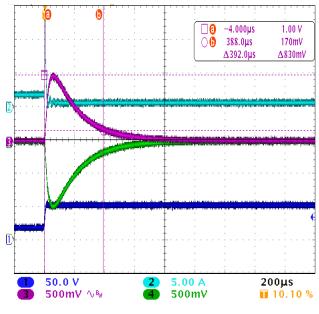
Typical Performance Plots: 25°C case, 28 Vin, 100% load, free run, unless otherwise specified. For reference only, not guaranteed specifications.



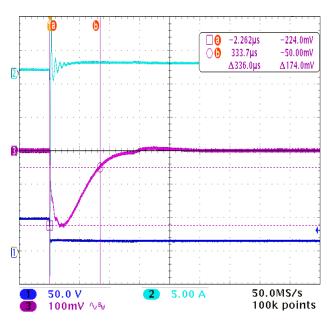
MFK2815D STEP LOAD 50% - 100% FIGURE 18



MFK2815D STEP LOAD 100% - 50% FIGURE 19



MFK2815D STEP LINE 16 - 50 VOLTS
FIGURE 20

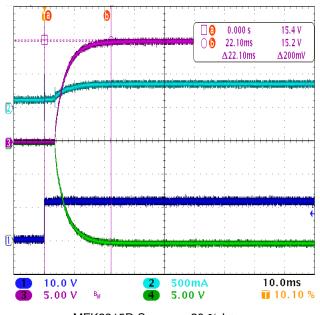


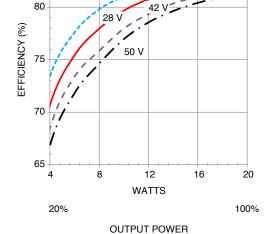
MFK2815D STEP LINE 50 - 16 VOLTS
FIGURE 21

### **16 TO 50 VOLTS INPUT - 25 WATT**

Typical Performance Plots: 25°C case, 28 Vin, 100% load, free run, unless otherwise specified. For reference only, not guaranteed specifications.

85





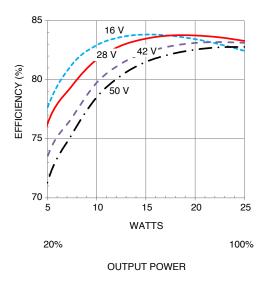
16 V

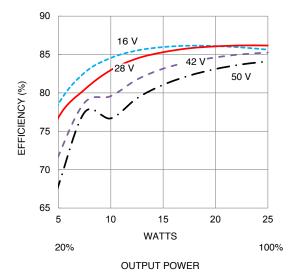
MFK2815D START-UP 20 % LOAD

FIGURE 22

MFK2805D EFFICIENCY

FIGURE 23





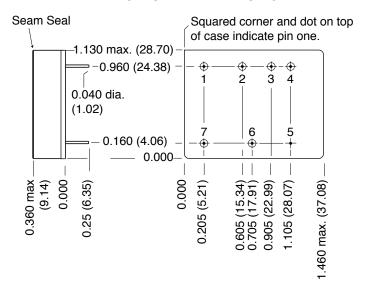
MFK2812D EFFICIENCY
FIGURE 24

MFK2815D EFFICIENCY
FIGURE 25

MFK2807D Efficiency will be added in the future.

### 16 TO 50 VOLTS INPUT - 25 WATT

#### **BOTTOM VIEW MFK SINGLE**



Weight: 38 grams maximum

### Case dimensions in inches (mm)

Tolerance ±0.005 (0.13) for three decimal places ±0.01 (0.3) for two decimal places unless otherwise specified

#### **CAUTION**

Heat from reflow or wave soldering may damage the device. Solder pins individually with heat application not exceeding 300°C for 10 seconds per pin.

### Materials

Header Cold Rolled Steel/Nickel/Gold

Cover Kovar/Nickel

Pins #52 alloy/Gold ceramic seal

Gold plating of 50 - 150 microinches included in pin diameter

Seal Hole: 0.123 ±0.002 (3.12 ±0.05)

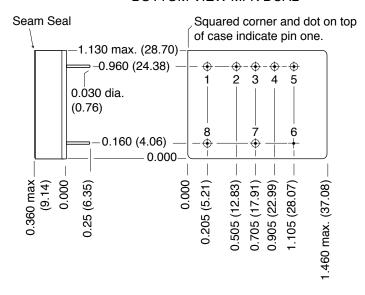
Case E5. Rev G. 2015.04.21

Please refer to the numerical dimensions for accuracy.

FIGURE 26: MFK SINGLE OUTPUT EXCEPT 28 VOLT SINGLE

### 16 TO 50 VOLTS INPUT - 25 WATT

#### **BOTTOM VIEW MFK DUAL**



Weight: 38 grams maximum

### Case dimensions in inches (mm)

Tolerance ±0.005 (0.13) for three decimal places ±0.01 (0.3) for two decimal places unless otherwise specified

#### **CAUTION**

Heat from reflow or wave soldering may damage the device. Solder pins individually with heat application not exceeding 300°C for 10 seconds per pin.

### **Materials**

Header Cold Rolled Steel/Nickel/Gold

Cover Kovar/Nickel

Pins #52 alloy/Gold ceramic seal

Gold plating of 50 - 150 microinches included in pin diameter

Seal Hole: 0.091 ±0.002 (2.31 ±0.05)

Case E6. Rev F. 2015.04.21

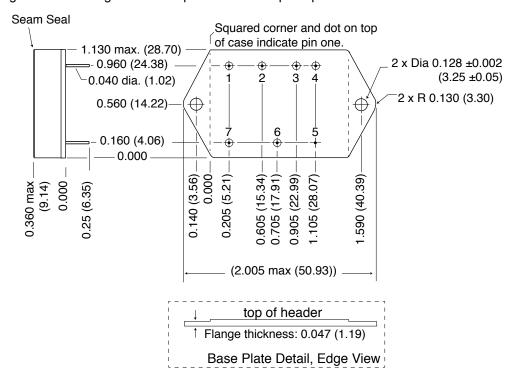
Please refer to the numerical dimensions for accuracy.

FIGURE 27: MFK DUAL OUTPUT INCLUDES 28 VOLT SINGLE

### 16 TO 50 VOLTS INPUT - 25 WATT

#### **BOTTOM VIEW MFK SINGLE FLANGED**

Flanged cases: Designator "F" required in Case Option position of model number



Weight: 38 grams maximum

### Case dimensions in inches (mm)

Tolerance  $\pm 0.005$  (0.13) for three decimal places  $\pm 0.01$  (0.3) for two decimal places

unless otherwise specified

#### **CAUTION**

Heat from reflow or wave soldering may damage the device. Solder pins individually with heat application not exceeding 300°C for 10 seconds per pin.

#### **Materials**

Header Cold Rolled Steel/Nickel/Gold

Cover Kovar/Nickel

Pins #52 alloy/Gold compression glass seal.

Gold plating of 50 - 150 microinches included in pin diameter

Seal Hole: 0.123 ±0.002 (3.12 ±0.05)

Case G5, Rev F, 2015.04.21

Please refer to the numerical dimensions for accuracy.

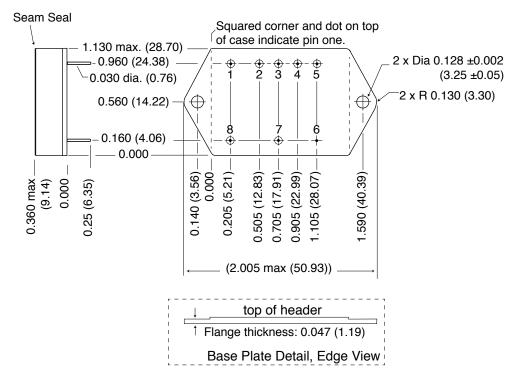
FIGURE 28: MFK SINGLE OUTPUT FLANGED

**EXCEPT 28 VOLT SINGLE** 

### 16 TO 50 VOLTS INPUT - 25 WATT

### BOTTOM VIEW CASE MFK DUAL FLANGED

Flanged cases: Designator "F" required in Case Option position of model number



Weight: 38 grams maximum

### Case dimensions in inches (mm)

Tolerance  $\pm 0.005$  (0.13) for three decimal places  $\pm 0.01$  (0.3) for two decimal places unless otherwise specified

### **CAUTION**

Heat from reflow or wave soldering may damage the device. Solder pins individually with heat application not exceeding 300°C for 10 seconds per pin.

#### **Materials**

Header Cold Rolled Steel/Nickel/Gold

Cover Kovar/Nickel

Pins #52 alloy/Gold compression glass seal.

Gold plating of 50 - 150 microinches included in pin diameter

Seal Hole: 0.123 ±0.002 (3.12 ±0.05)

Case G6, Rev F, 2015.04.21

Please refer to the numerical dimensions for accuracy.

FIGURE 29: MFK DUAL OUTPUT FLANGED INCLUDES 28 VOLT SINGLE

### **16 TO 50 VOLTS INPUT - 25 WATT**

# STANDARD, /ES (NON-QML) AND /883 (CLASS H, QML) MIL-PRF-38534 ELEMENT EVALUATION

	NON-QML 1	QI	ИL
COMPONENT-LEVEL TEST PERFORMED	STANDARD AND /ES	_	ss H 83
	M/S <sup>2</sup>	M/S <sup>2</sup>	P 3
Element Electrical	•	•	•
Visual		•	•
Internal Visual			
Final Electrical			•
Wire Bond Evaluation			

#### Notes

- 1. Standard and /ES non-QML products may not meet all of the requirements of MIL-PRF-38534.
- 2. M/S = Active components (microcircuit and semiconductor die)
- P = Passive components, Class H element evaluation. Not applicable to standard and /ES element evaluation.

TABLE 11: ELEMENT EVALUATION

### 16 TO 50 VOLTS INPUT - 25 WATT

# STANDARD, /ES (NON-QML) AND /883 (CLASS H, QML) MIL-PRF-38534 ENVIRONMENTAL SCREENING

	NON-QM	L <sup>1</sup>	QML <sup>2</sup>	
TEST PERFORMED	STANDARD	/ES	CLASS H /883	
Pre-cap Inspection, Method 2017, 2032		•		
Temperature Cycle (10 times)				
Method 1010, Cond. C, -65°C to +150°C, ambient			-	
Method 1010, Cond. B, -55°C to +125°C, ambient		-		
Constant Acceleration				
Method 2001, 3000 g			•	
Method 2001, 500 g		-		
PIND, Test Method 2020, Cond. A		-	■ 3	
Burn-in Method 1015, +125°C case, typical <sup>4</sup>				
96 hours		-		
160 hours				
Final Electrical Test, MIL-PRF-38534, Group A,				
Subgroups 1 through 6, -55°C, +25°C, +125°C case			•	
Subgroups 1 and 4, +25°C case		-		
Hermeticity Test				
Gross Leak, Method 1014, Cond. C		-	•	
Fine Leak, Method 1014, Cond. A		-	•	
Gross Leak, Dip	•			
Final visual inspection, Method 2009	•	-	•	

Test methods are referenced to MIL-STD-883 as determined by MIL-PRF-38534.

#### Notes

- 1. Standard and /ES, non-QML products, may not meet all of the requirements of MIL-PRF-38534.
- 2. All processes are QML qualified and performed by certified operators.
- 3. Not required by DLA but performed to assure product quality.
- 4. Burn-in temperature designed to bring the case temperature to +125°C minimum. Burn-in is a powered test.

TABLE 12: ENVIRONMENTAL SCREENING

