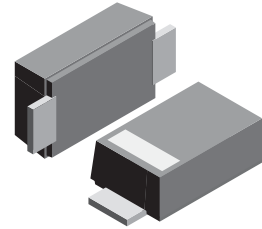


VOLTAGE RANGE: 20 - 100V
CURRENT: 2.0 A

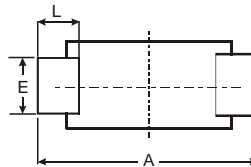
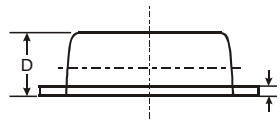
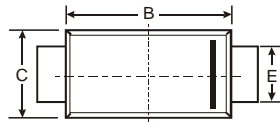


Features

- Schottky Barrier Chip
- Ideally Suited for Automatic Assembly
- Low Power Loss, High Efficiency
- For Use in Low Voltage Application
- Guard Ring Die Construction
- Plastic Case Material has UL Flammability Classification Rating 94V-O

Mechanical Data

- Case: SOD-123FL
plastic body over passivated junction
- Terminals : Plated axial leads,
- solderable per MIL-STD-750, Method 2026
- Polarity : Color band denotes cathode end
- Mounting Position : Any
- Weight: 0.0007 ounce, 0.02 grams



SOD-123FL			
Dim	Min	Max	Typ
A	3.58	3.72	3.65
B	2.72	2.78	2.75
C	1.77	1.83	1.80
D	1.02	1.08	1.05
E	0.097	1.03	1.00
H	0.13	0.17	0.15
L	0.53	0.57	0.55
All Dimensions in mm			



Maximum Ratings and Electrical Characteristics T_A = 25°C unless otherwise specified

Single phase, half wave, 60Hz, resistive or inductive load. For capacitive load, derate current by 20%.

Characteristic	Symbol	SK22L	SK23L	SK24L	SK25L	SK26L	SK28L	SK210L	Unit
Maximum repetitive peak reverse voltage	V _{RRM}	20	30	40	50	60	80	100	V
Maximum RMS voltage	V _{RMS}	14	21	28	35	42	56	70	V
Maximum DC blocking voltage	V _{DC}	20	30	40	50	60	80	100	V
Maximum average forward rectified current at T _L (see fig.1)	I _(AV)	2.0							A
Peak forward surge current 8.3ms single half sine-wave superimposed on rated load (JEDEC Method)	I _{FSM}	50.0							A
Maximum instantaneous forward voltage at 2.0A	V _F	0.55		0.70		0.85		V	
Maximum DC reverse current at rated DC blocking voltage	I _R	20			10			mA	
Typical junction capacitance (NOTE 1)	C _J	220			180			pF	
Typical thermal resistance (NOTE 2)	R _{θJA}	75.0							°C/W
Operating junction temperature range	T _J	-65 to +125			-65 to +150			°C	
Storage temperature range	T _{STG}	-65 to +150							°C

Note: 1. Measured at 1MHz and applied reverse voltage of 4.0V D.C.
 2. P.C.B. mounted with 0.2x0.2" (5.0x5.0mm) copper pad areas

RATINGS AND CHARACTERISTIC CURVES SK22L THRU SK210L

