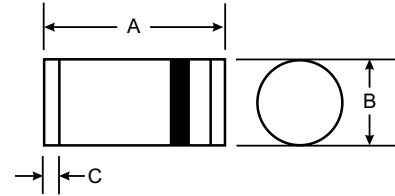


**VOLTAGE RANGE: 20 - 60V**  
**CURRENT: 2.0 A**

### Features

- Schottky Barrier Chip
- Ideally Suited for Automatic Assembly
- Low Power Loss, High Efficiency
- For Use in Low Voltage Application
- Guard Ring Die Construction
- Plastic Case Material has UL Flammability Classification Rating 94V-O



### Mechanical Data

- Case: LL41/DO-213
- Terminals: Solder Plated, Solderable per MIL-STD-750, Method 2026
- Polarity: Cathode Band or Cathode Notch



LL41/ DO-213AB		
Dim	Min	Max
A	4.80	5.20
B	2.40	2.60
C	0.55 Nominal	
All Dimensions in mm		

### Maximum Ratings and Electrical Characteristics T<sub>A</sub> = 25°C unless otherwise specified

Single phase, half wave, 60Hz, resistive or inductive load. For capacitive load, derate current by 20%.

Characteristic	Symbol	SM220	SM230	SM240	SM250	SM260	Unit	
Peak Repetitive Reverse Voltage	V <sub>RRM</sub>							
Working Peak Reverse Voltage	V <sub>RWM</sub>	20	30	40	50	60	V	
DC Blocking Voltage	V <sub>R</sub>							
RMS Reverse Voltage	V <sub>R(RMS)</sub>	14	21	28	35	42	V	
Average Rectified Output Current @T <sub>L</sub> = 105°C	I <sub>O</sub>	2.0						A
Non-Repetitive Peak Forward Surge Current 8.3ms Single half sine-wave superimposed on rated load (JEDEC Method)	I <sub>FSM</sub>	50						A
Forward Voltage @I <sub>F</sub> = 2.0A	V <sub>FM</sub>	0.50				0.70	V	
Peak Reverse Current @T <sub>A</sub> = 25°C At Rated DC Blocking Voltage @T <sub>A</sub> = 100°C	I <sub>RM</sub>	0.5				20	mA	
Typical Thermal Resistance (Note 1)	R <sub>θJL</sub> R <sub>θJA</sub>	17 75					°C/W	
Operating Temperature Range	T <sub>j</sub>	-65 to +125					°C	
Storage Temperature Range	T <sub>STG</sub>	-65 to +150					°C	

Note: 1. Mounted on P.C. Board with 8.0mm<sup>2</sup> copper pad area.

## RATING AND CHARACTERISTIC CURVES ( SM220 THRU SM260 )

FIG. 1 - TYPICAL FORWARD CURRENT DERATING CURVE

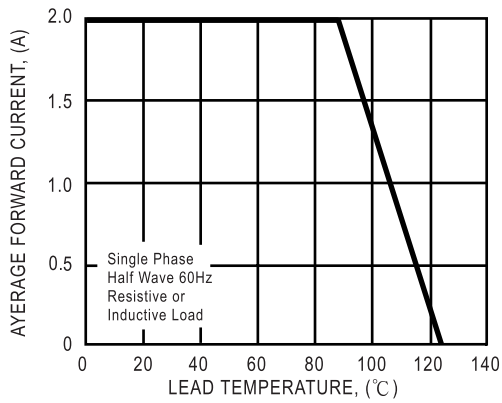


FIG. 2 - TYPICAL INSTANTANEOUS FORWARD CHARACTERISTICS

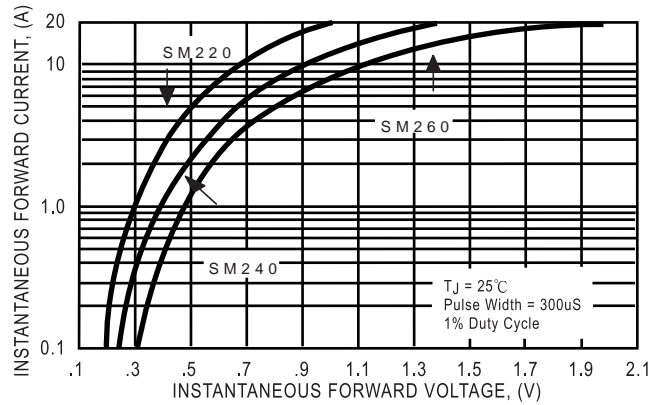


FIG. 3 - TYPICAL REVERSE CHARACTERISTICS

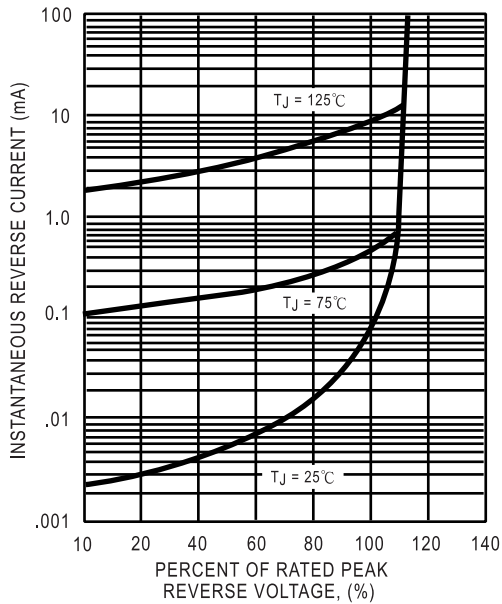


FIG. 4 - MAXIMUM NON-REPETITIVE FORWARD SURGE CURRENT

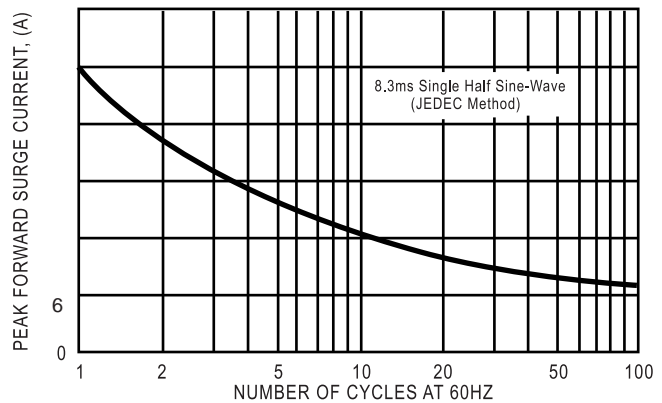


FIG. 5 - TYPICAL JUNCTION CAPACITANCE

