

**VOLTAGE RANGE: 50 - 1000V**

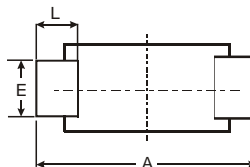
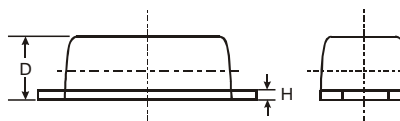
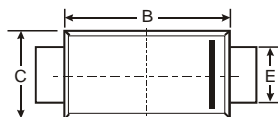
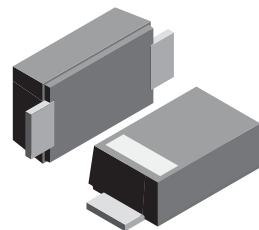
**CURRENT: 1.0 A**

### Features

- Glass Passivated Die Construction
- Ideally Suited for Automatic Assembly
- Low Forward Voltage Drop
- Low Power Loss
- Built-in Strain Relief
- Plastic Case Material has UL Flammability Classification Rating 94V-O

### Mechanical Data

- Case: JEDEC SOD-123FL molded plastic body over passivated chip
- Terminals : Plated axial leads, solderable per MIL-STD-750, Method 2026
- Polarity : Color band denotes cathode end
- Mounting Position : Any
- Weight:0.0007 ounce, 0.02 grams



SOD-123FL			
Dim	Min	Max	Typ
A	3.58	3.72	3.65
B	2.72	2.78	2.75
C	1.77	1.83	1.80
D	1.02	1.08	1.05
E	0.097	1.03	1.00
H	0.13	0.17	0.15
L	0.53	0.57	0.55
All Dimensions in mm			

### Maximum Ratings and Electrical Characteristics $T_A = 25^\circ\text{C}$ unless otherwise specified

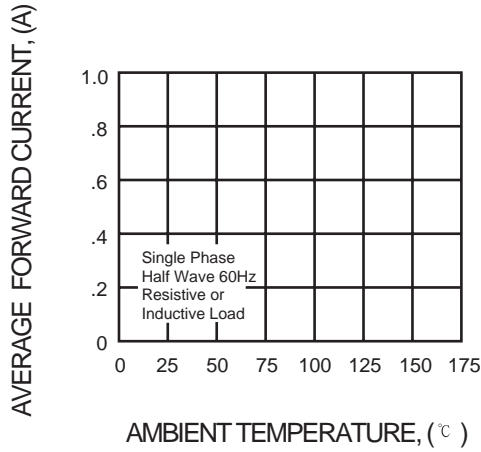
Single phase, half wave, 60Hz, resistive or inductive load. For capacitive load, derate current by 20%.

Characteristic	Symbol	SOD4001	SOD4002	SOD4003	SOD4004	SOD4005	SOD4006	SOD4007	Unit
	Marking	D1	D2	D3	D4	D5	D6	D7	
Peak Repetitive Reverse Voltage	$V_{RRM}$								V
Working Peak Reverse Voltage	$V_{RWM}$	50	100	200	400	600	800	1000	
DC Blocking Voltage	$V_R$								
RMS Reverse Voltage	$V_{R(RMS)}$	35	70	140	280	420	560	700	V
Average Rectified Output Current @ $T_L = 100^\circ\text{C}$	$I_O$	1.0							A
Non-Repetitive Peak Forward Surge Current 8.3ms Single half sine-wave superimposed on rated load (JEDEC Method)	$I_{FSM}$	30							A
Forward Voltage @ $I_F = 1.0\text{A}$	$V_{FM}$	1.10							V
Peak Reverse Current @ $T_A = 25^\circ\text{C}$ At Rated DC Blocking Voltage @ $T_A = 125^\circ\text{C}$	$I_{RM}$	5.0 200							$\mu\text{A}$
Reverse Recovery Time (Note 1)	$t_{rr}$	2.5							$\mu\text{s}$
Typical Junction Capacitance (Note 2)	$C_j$	15							pF
Typical Thermal Resistance (Note 3)	$R_{\theta JL}$	30							K/W
Operating and Storage Temperature Range	$T_j, T_{STG}$	-65 to +175							$^\circ\text{C}$

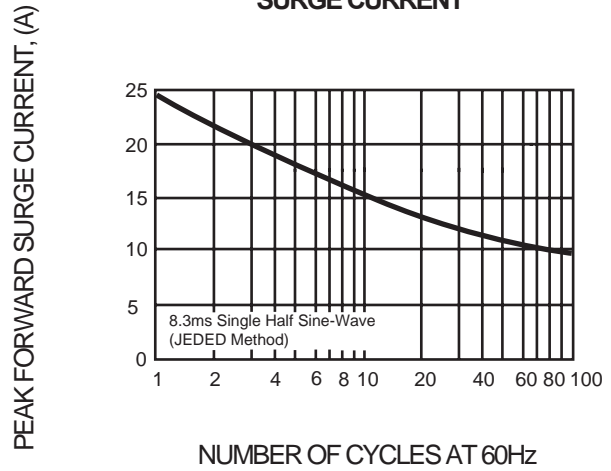
Note: 1. Measured with  $I_F = 0.5\text{A}$ ,  $I_R = 1.0\text{A}$ ,  $I_{rr} = 0.25\text{A}$ ,  
 2. Measured at 1.0 MHz and applied reverse voltage of 4.0 V DC.  
 3. Mounted on P.C. Board with 8.0mm<sup>2</sup> land area.



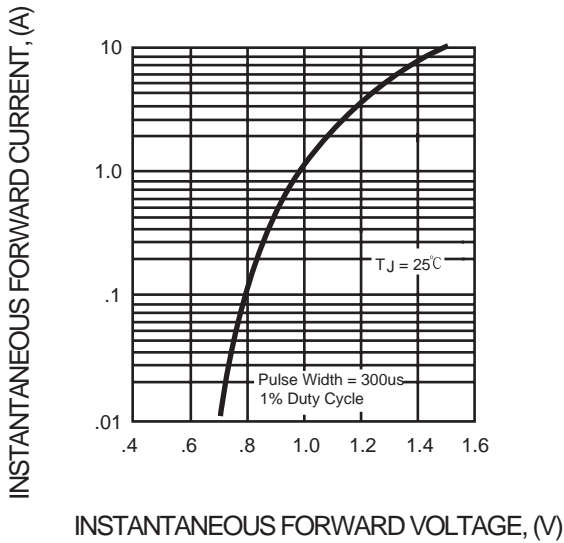
**FIG. 1 - TYPICAL FORWARD CURRENT DERATING CURVE**



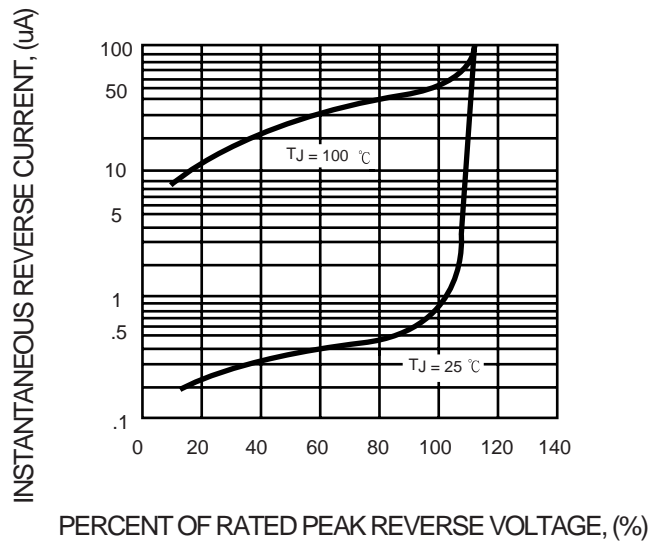
**FIG. 2 - MAXIMUM NON-REPETITIVE FORWARD SURGE CURRENT**



**FIG. 3 - TYPICAL INSTANTANEOUS FORWARD CHARACTERISTICS**



**FIG. 4 - TYPICAL REVERSE CHARACTERISTICS**



**FIG. 5 - TYPICAL JUNCTION CAPACITANCE**

