

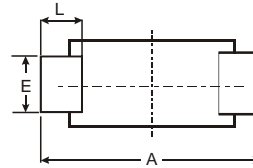
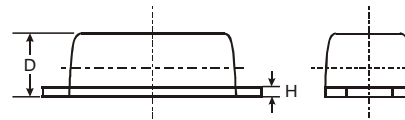
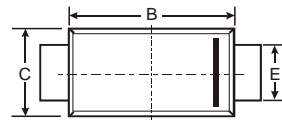
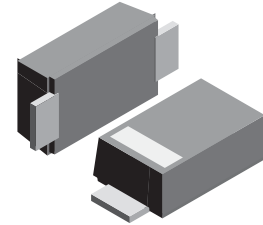
**VOLTAGE RANGE: 50 - 1000V**  
**CURRENT: 1.0A**

### Features

- Glass passivated device
- Ideal for surface mounted applications
- Low reverse leakage
- Metallurgically bonded construction
- High temperature soldering guaranteed:  
 ● 250°C/10 seconds, 0.375(9.5mm) lead length,  
 5 lbs. (2.3kg) tension

### Mechanical Data

- Case: JEDEC SOD-123FL molded plastic body over passivated chip
- Terminals : Plated axial leads,
- solderable per MIL-STD-750, Method 2026
- Polarity : Color band denotes cathode end
- Mounting Position : Any
- Weight: 0.0007 ounce, 0.02 grams



SOD-123FL			
Dim	Min	Max	Typ
A	3.58	3.72	3.65
B	2.72	2.78	2.75
C	1.77	1.83	1.80
D	1.02	1.08	1.05
E	0.097	1.03	1.00
H	0.13	0.17	0.15
L	0.53	0.57	0.55
All Dimensions in mm			

### Maximum Ratings and Electrical Characteristics T<sub>A</sub> = 25°C unless otherwise specified

Single phase, half wave, 60Hz, resistive or inductive load. For capacitive load, derate current by 20%.

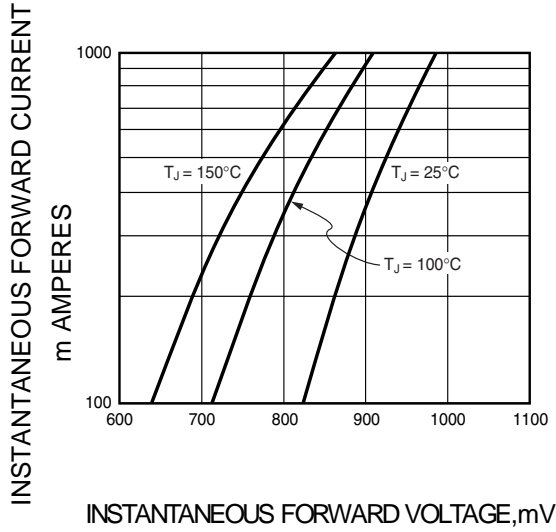
Characteristic	Symbol	DSR1A	DSR1B	DSR1D	DSR1G	DSR1J	DSR1K	DSR1M	Unit
	Marking	R1A	R1B	R1D	R1G	R1J	R1K	R1M	
Maximum repetitive peak reverse voltage	V <sub>RRM</sub>	50	100	200	400	600	800	1000	V
Maximum RMS voltage	V <sub>RMS</sub>	35	70	140	280	420	560	700	V
Maximum DC blocking voltage	V <sub>DC</sub>	50	100	200	400	600	800	1000	V
Maximum average forward rectified current at T <sub>A</sub> =65°C (NOTE 1)	I <sub(av)< sub=""></sub(av)<>	1.0							A
Peak forward surge current 8.3ms single half sine-wave superimposed on rated load (JEDEC Method) T <sub>L</sub> =25°C	I <sub>FSM</sub>	25.0							A
Maximum instantaneous forward voltage at 1.0A	V <sub>F</sub>	1.1							V
Maximum DC reverse current T <sub>A</sub> =25°C at rated DC blocking voltage T <sub>A</sub> =125°C	I <sub>R</sub>	10.0 50.0							μA
Typical junction capacitance (NOTE 2)	C <sub>J</sub>	4							pF
Typical thermal resistance (NOTE 3)	R <sub>θJA</sub>	180							K/W
Operating junction and storage temperature range	T <sub>J</sub> , T <sub>STG</sub>	-50 to +150							°C

- Note:** 1. Averaged over any 20ms period.  
 2. Measured at 1MHz and applied reverse voltage of 4.0V D.C.  
 3. Thermal resistance from junction to ambient at 0.375" (9.5mm) lead length, P.C.B. mounted

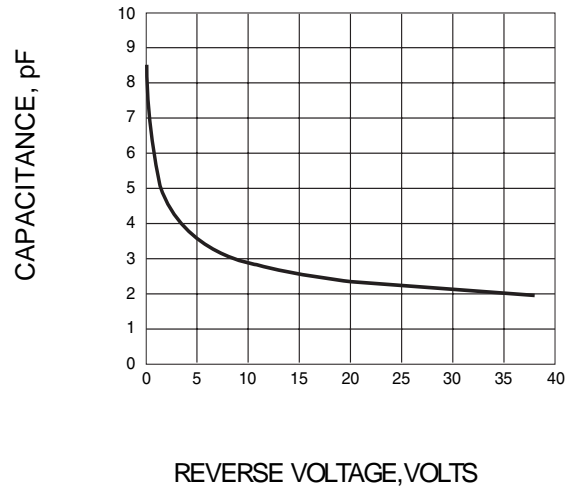


**RATINGS AND CHARACTERISTIC CURVES DSR1A THRU DSR1M**

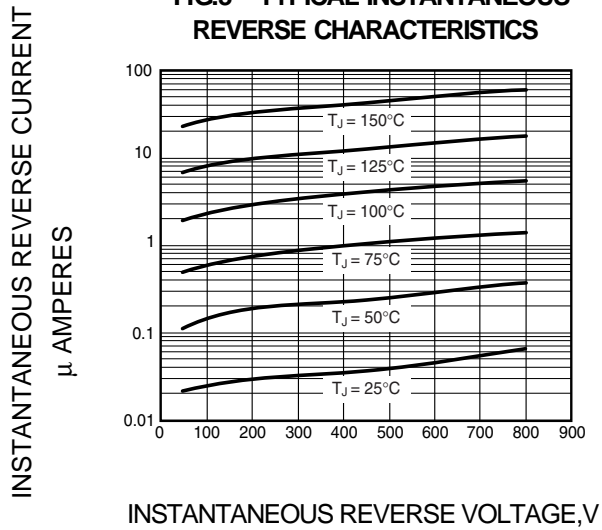
**FIG.1 – TYPICAL FORWARD CHARACTERISTIC**



**FIG.2 – TYPICAL JUNCTION CAPACITANCE**



**FIG.3 – TYPICAL INSTANTANEOUS REVERSE CHARACTERISTICS**



**FIG.4 – FORWARD DERATING CURVE**

