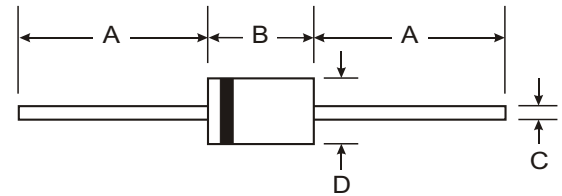
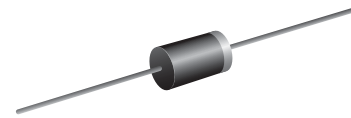


**VOLTAGE RANGE: 200 - 1000V**

**CURRENT: 0.25 -0.5A**



DO-41		
Dim	Min	Max
A	25.40	—
B	4.06	5.21
C	0.71	0.864
D	2.00	2.72
All Dimensions in mm		

### Features

- Low cost
- Diffused junction
- Low leakage
- Low forward voltage drop
- Easily cleaned with freon, Alcohol, Isopropand and similar solvents

### Mechanical Data

- Case: D O - 4 1 Molded Plastic
- Terminals: Plated Leads Solderable per MIL-STD-202, Method 208
- Polarity: Cathode Band
- Weight: 0.34 grams (approx.)
- Mounting Position: Any
- Marking: Type Number



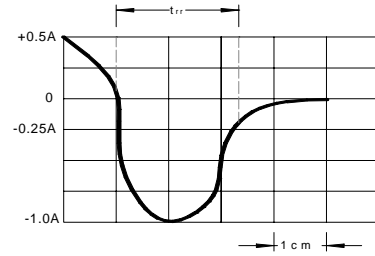
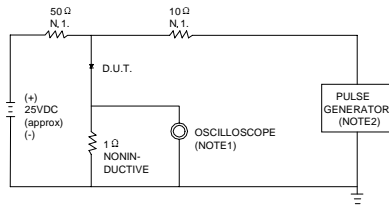
### Maximum Ratings and Electrical Characteristics T<sub>A</sub> = 25°C unless otherwise specified

Single phase, half wave, 60Hz, resistive or inductive load. For capacitive load, derate current by 20%.

Characteristic	Symbol	EU1Z	EU1	EU1A	EU1C	Unit
Maximum peak repetitive reverse voltage	V <sub>RRM</sub>	200	400	600	1000	V
Maximum RMS voltage	V <sub>RMS</sub>	140	280	420	700	V
Maximum DC blocking voltage	V <sub>DC</sub>	200	400	600	1000	V
Maximum average forward rectified current 9.5mm lead length @T <sub>A</sub> =75°C	I <sub>F(AV)</sub>	0.25			0.5	A
Peak forward surge current 10ms single half-sine-wave superimposed on rated load @T <sub>J</sub> =125°C	I <sub>FSM</sub>	15.0				A
Maximum instantaneous forward voltage @ I <sub>F</sub> =I <sub>F(AV)</sub>	V <sub>F</sub>	2.5				V
Maximum reverse current at Rated DC blocking voltage @T <sub>A</sub> =25°C @T <sub>A</sub> =100°C	I <sub>R</sub>	10.0 150.0				μ A
Maximum reverse recovery time (Note1)	t <sub>rr</sub>	100				ns
Typical junction capacitance (Note2)	C <sub>J</sub>	20	15			pF
Typical thermal resistance (Note3)	R <sub>θJL</sub>	17				°C/W
Operating junction temperature range	T <sub>J</sub>	- 55 ----- + 150				°C
Storage temperature range	T <sub>STG</sub>	- 55 ----- + 150				°C

NOTE: 1. Measured with I<sub>F</sub>=0.5A, I<sub>R</sub>=1A, I<sub>rr</sub>=0.25A.  
 2. Measured at 1.0MHz and applied reverse voltage of 4.0V DC.  
 3. Thermal resistance junction to ambient.

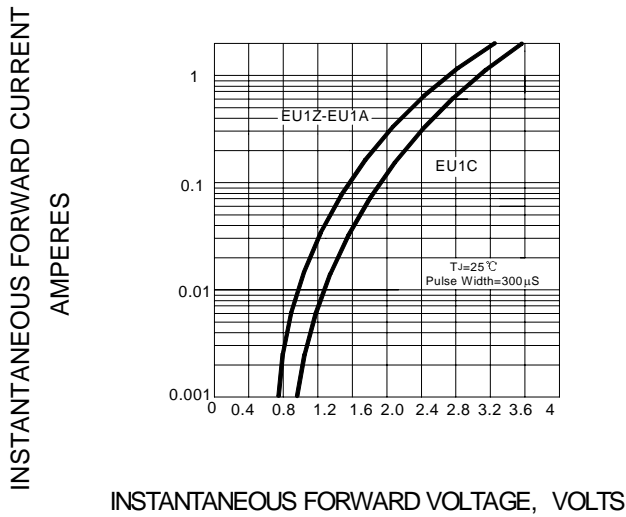
**FIG.1 – TEST CIRCUIT DIAGRAM AND REVERSE RECOVERY TIME CHARACTERISTIC**



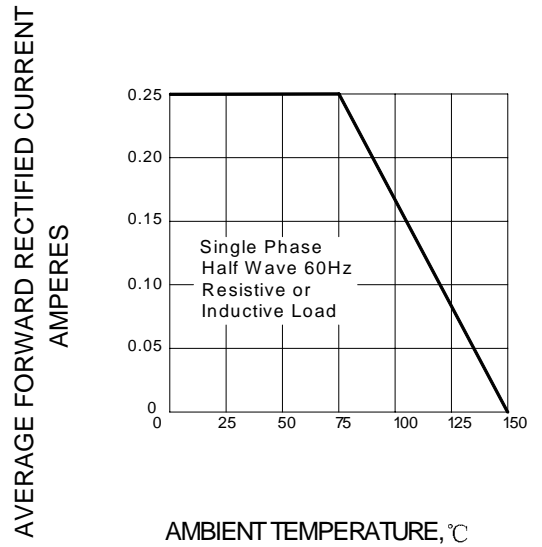
NOTES: 1. RISE TIME = 7ns MAX.INPUT IMPEDANCE = 1MΩ, 22pF.  
 2. RISE TIME = 10ns MAX.SOURCE IMPEDANCE = 50 Ω.

SET TIME BASE FOR 10/20 ns/cm

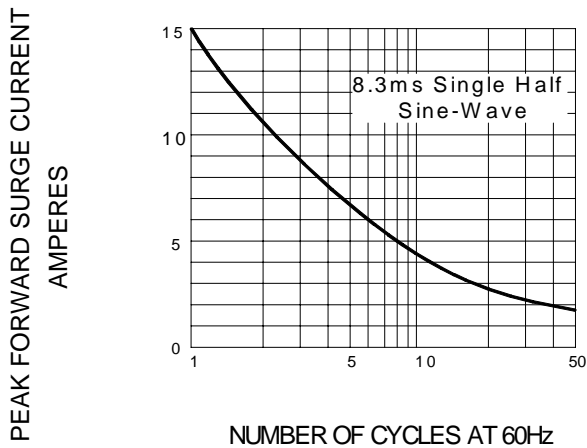
**FIG.2 – TYPICAL FORWARD CHARACTERISTIC**



**FIG.3 – FORWARD DERATING CURVE**



**FIG.4 – PEAK FORWARD SURGE CURRENT**



**FIG.3 – TYPICAL JUNCTION CAPACITANCE**

